

1/34

FIG. 1

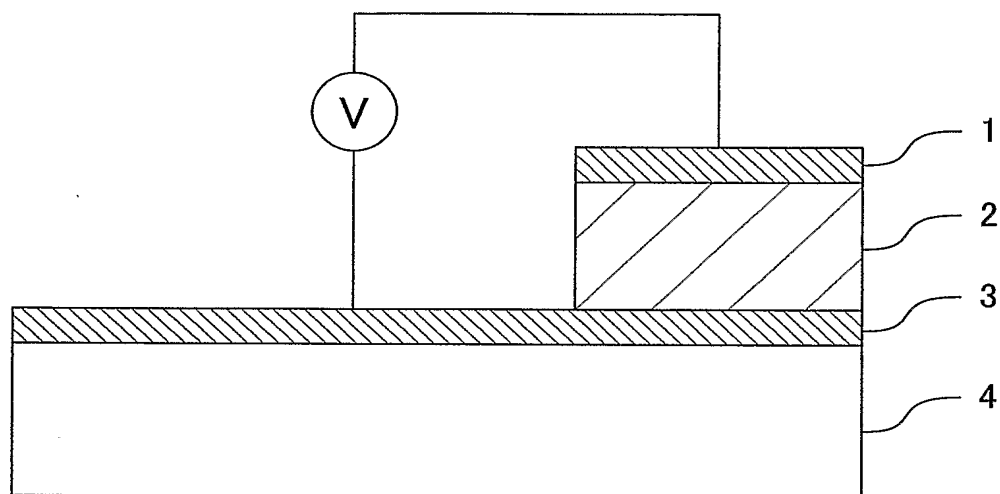


FIG. 2

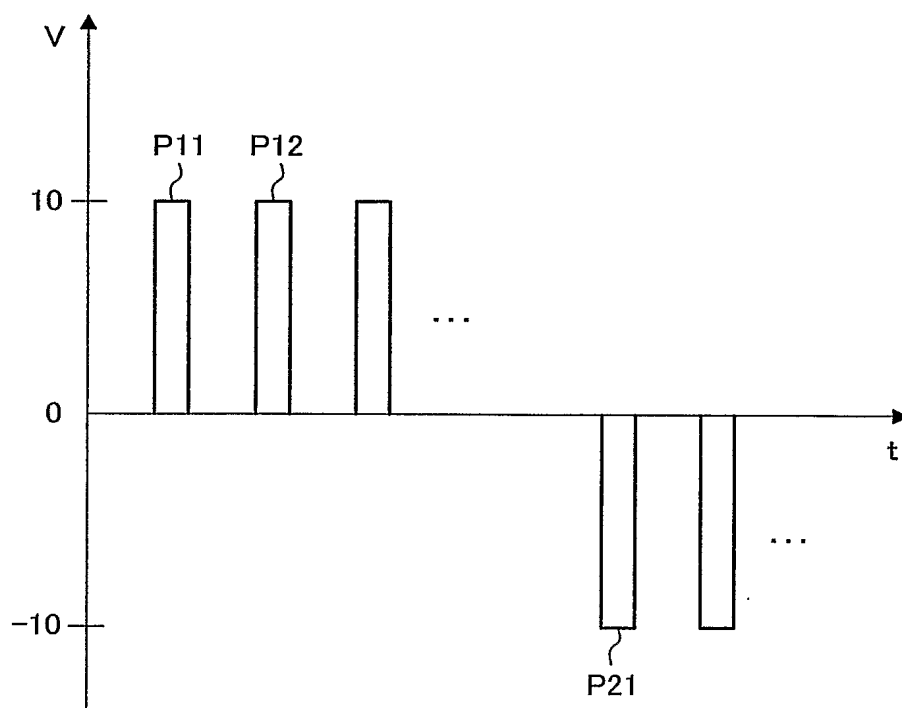


FIG. 3

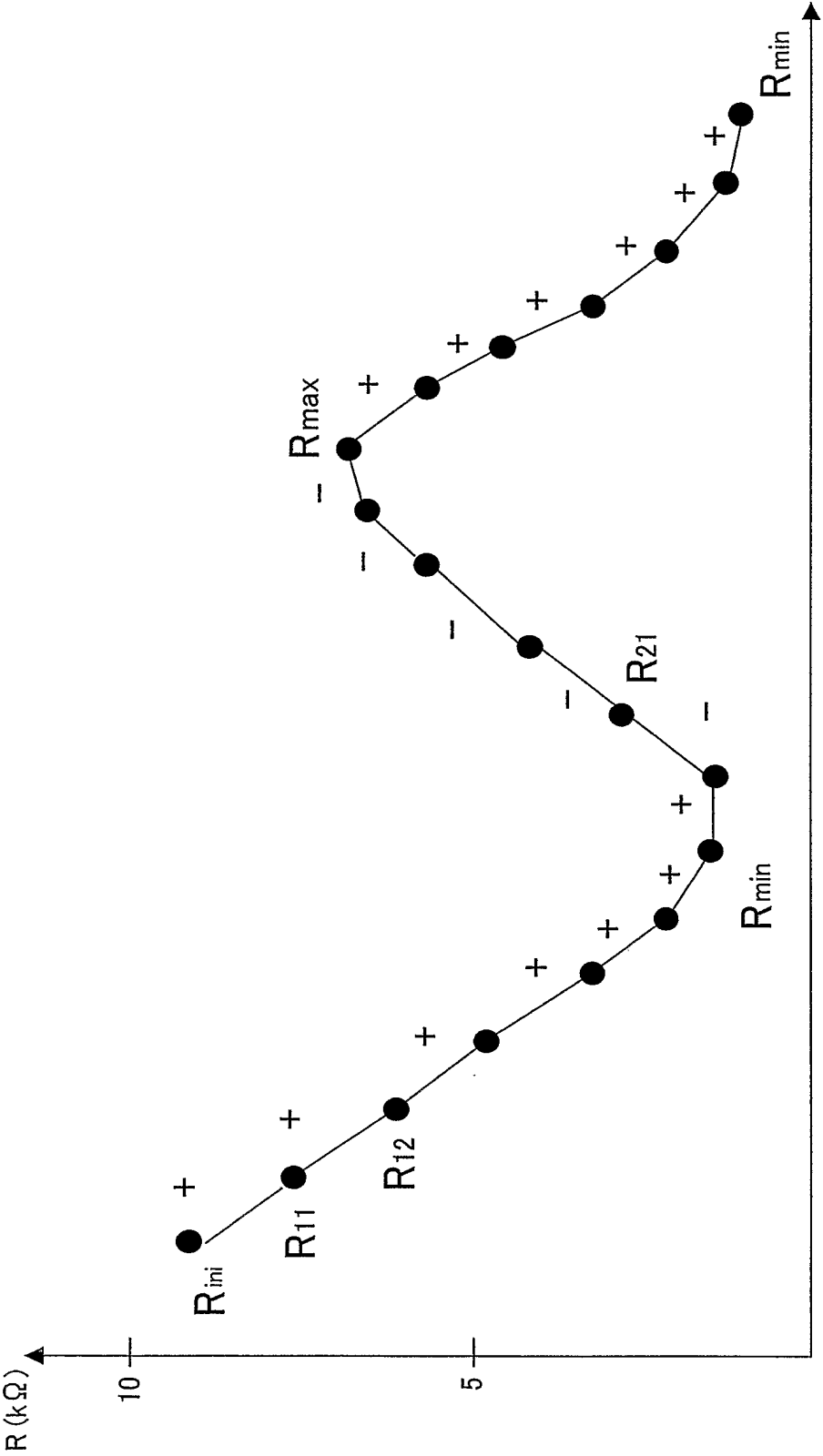


FIG. 4

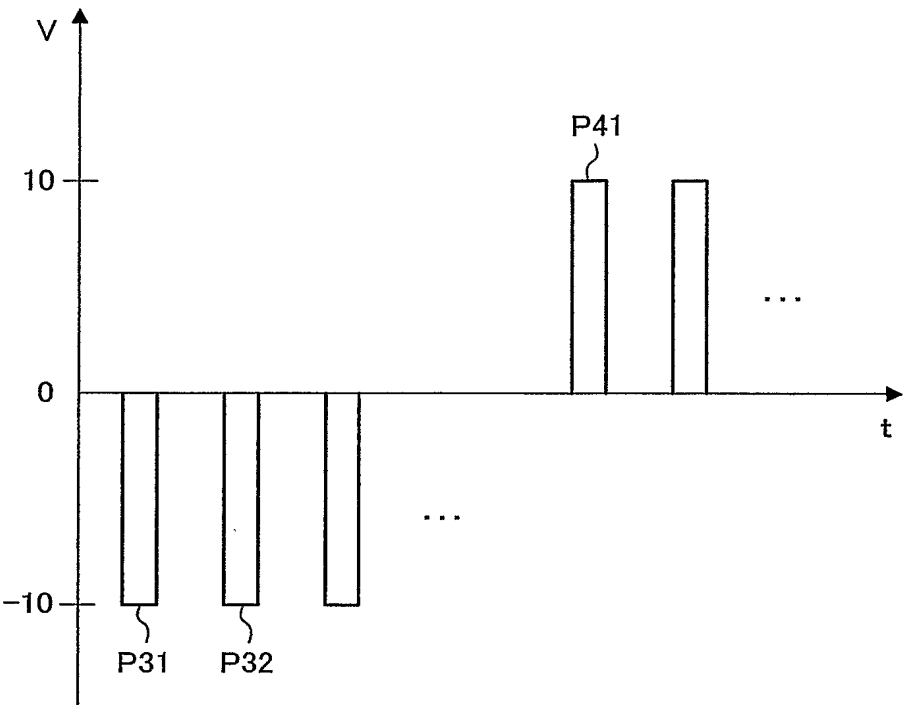
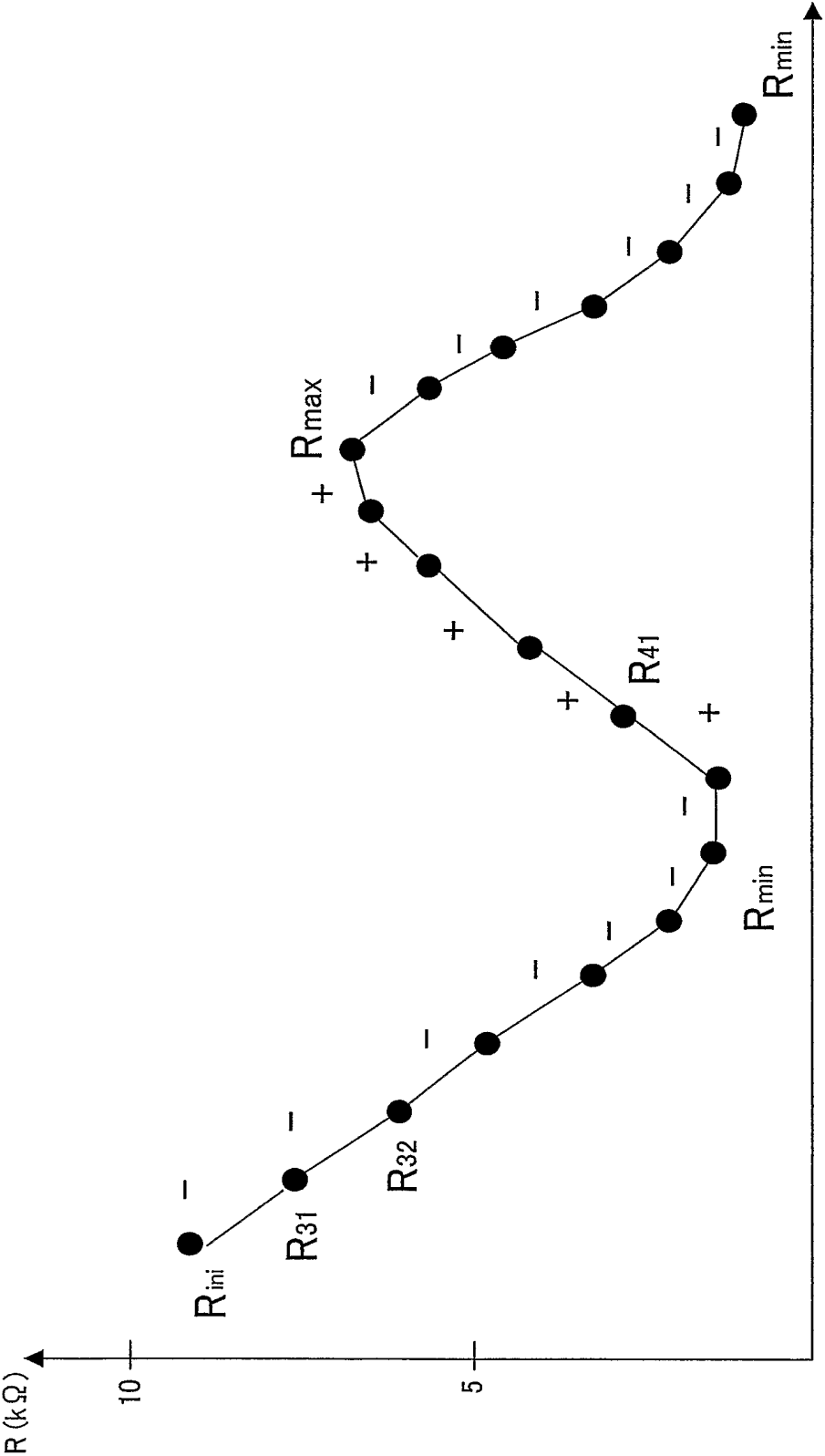


FIG. 5



5/34

FIG. 6

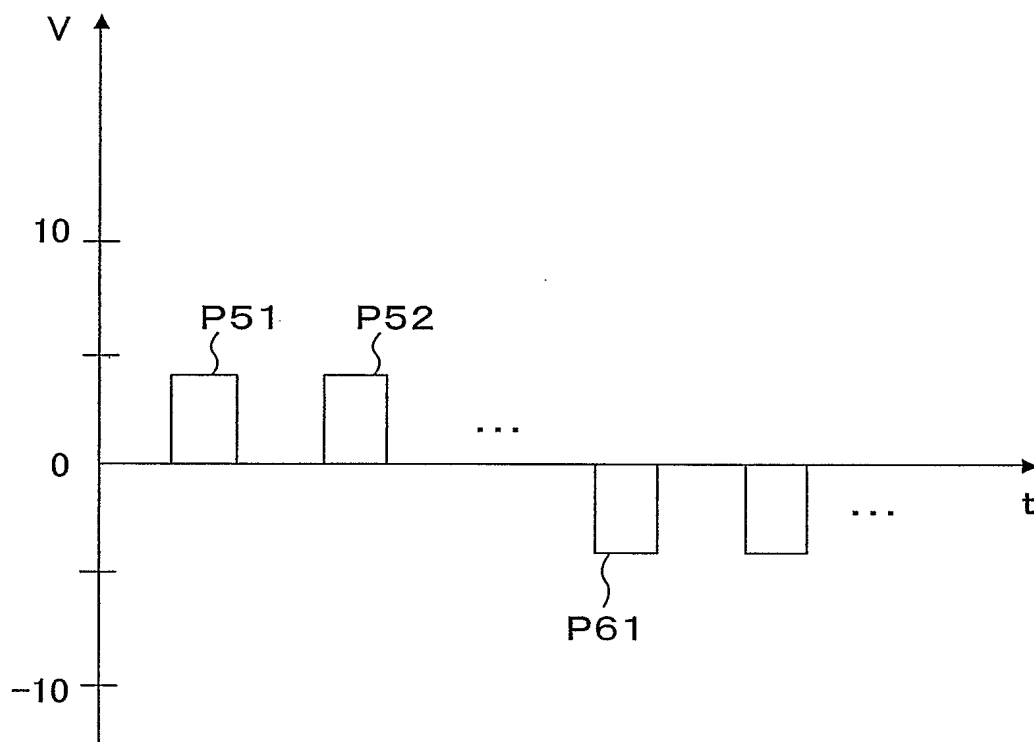
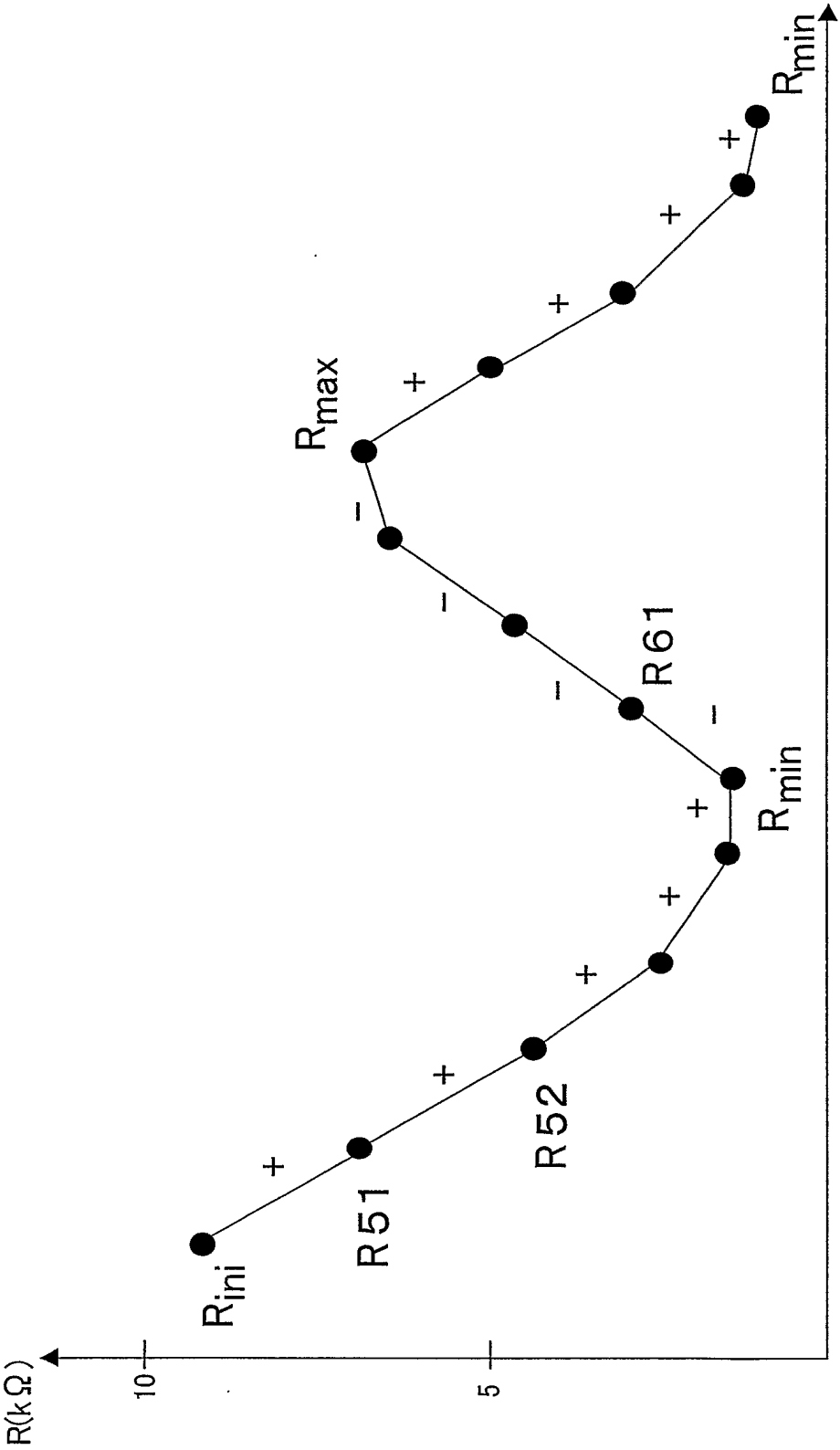


FIG. 7



7/34

FIG. 8

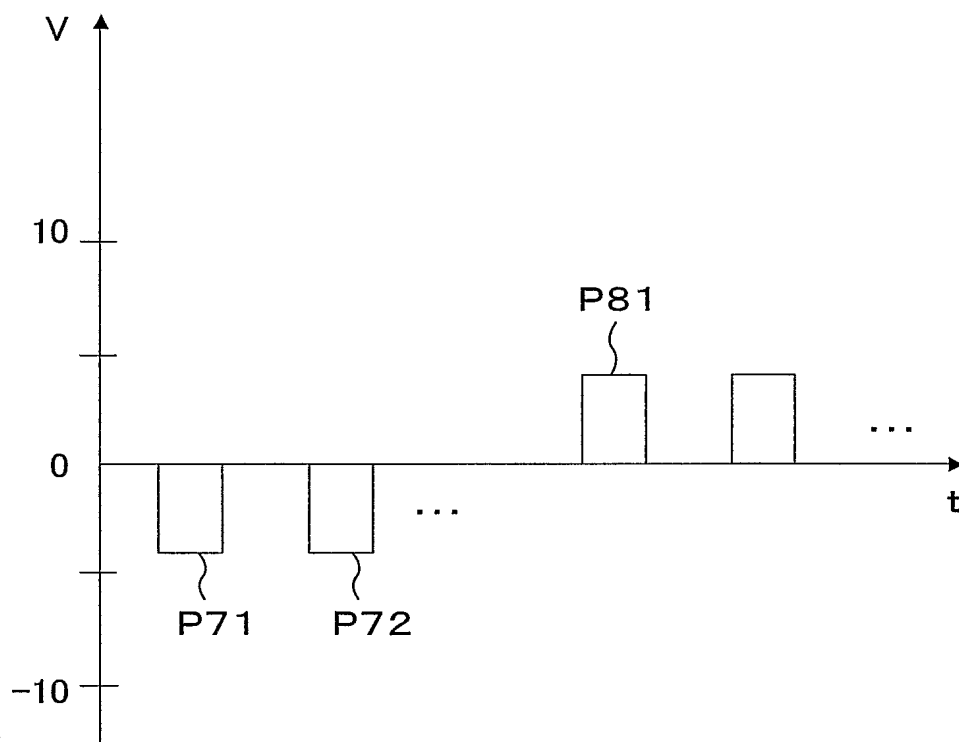
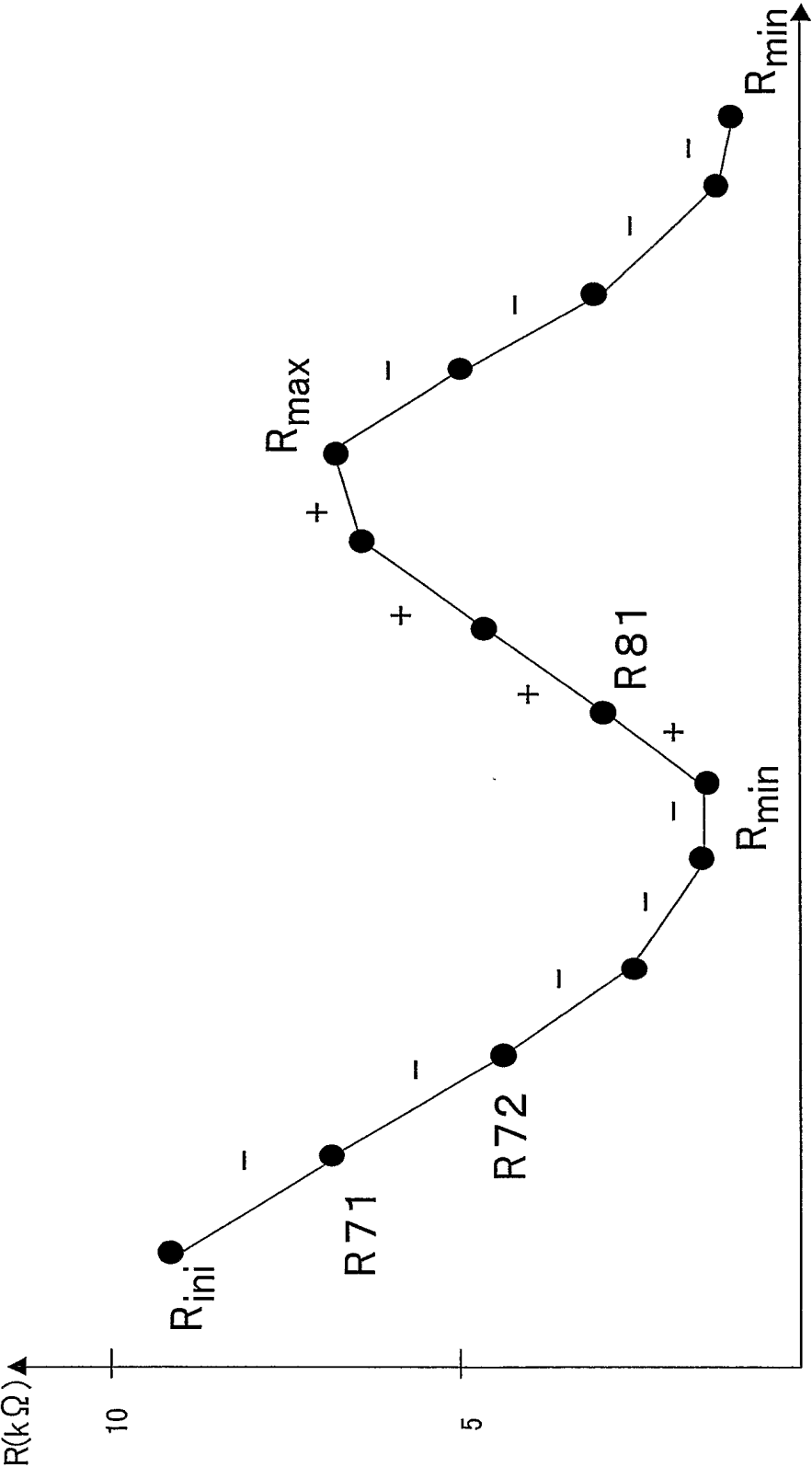
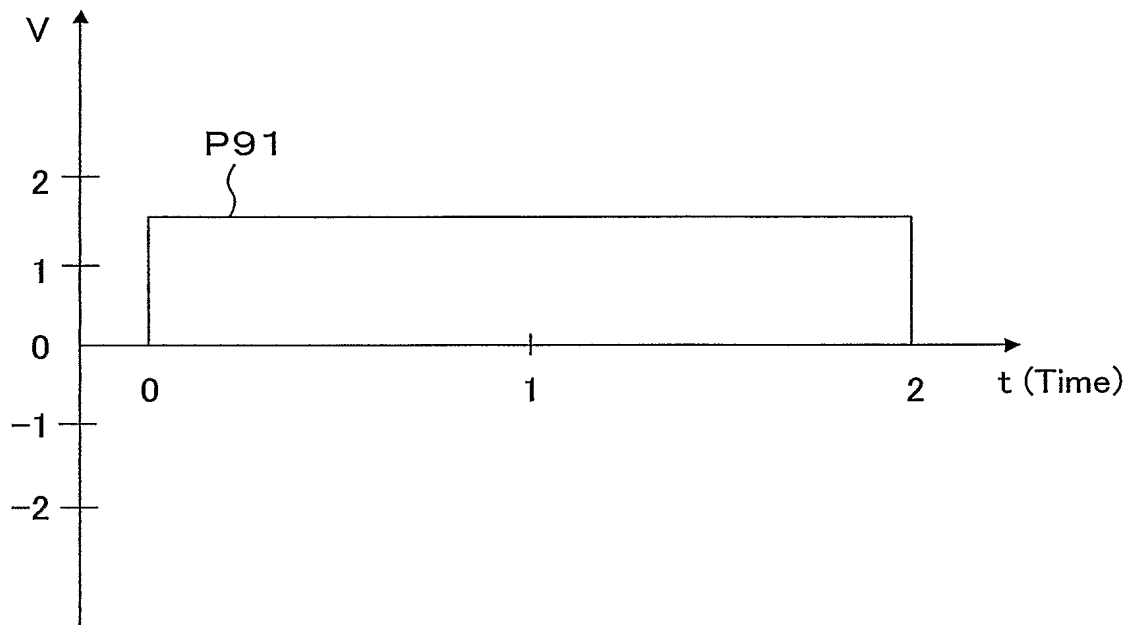


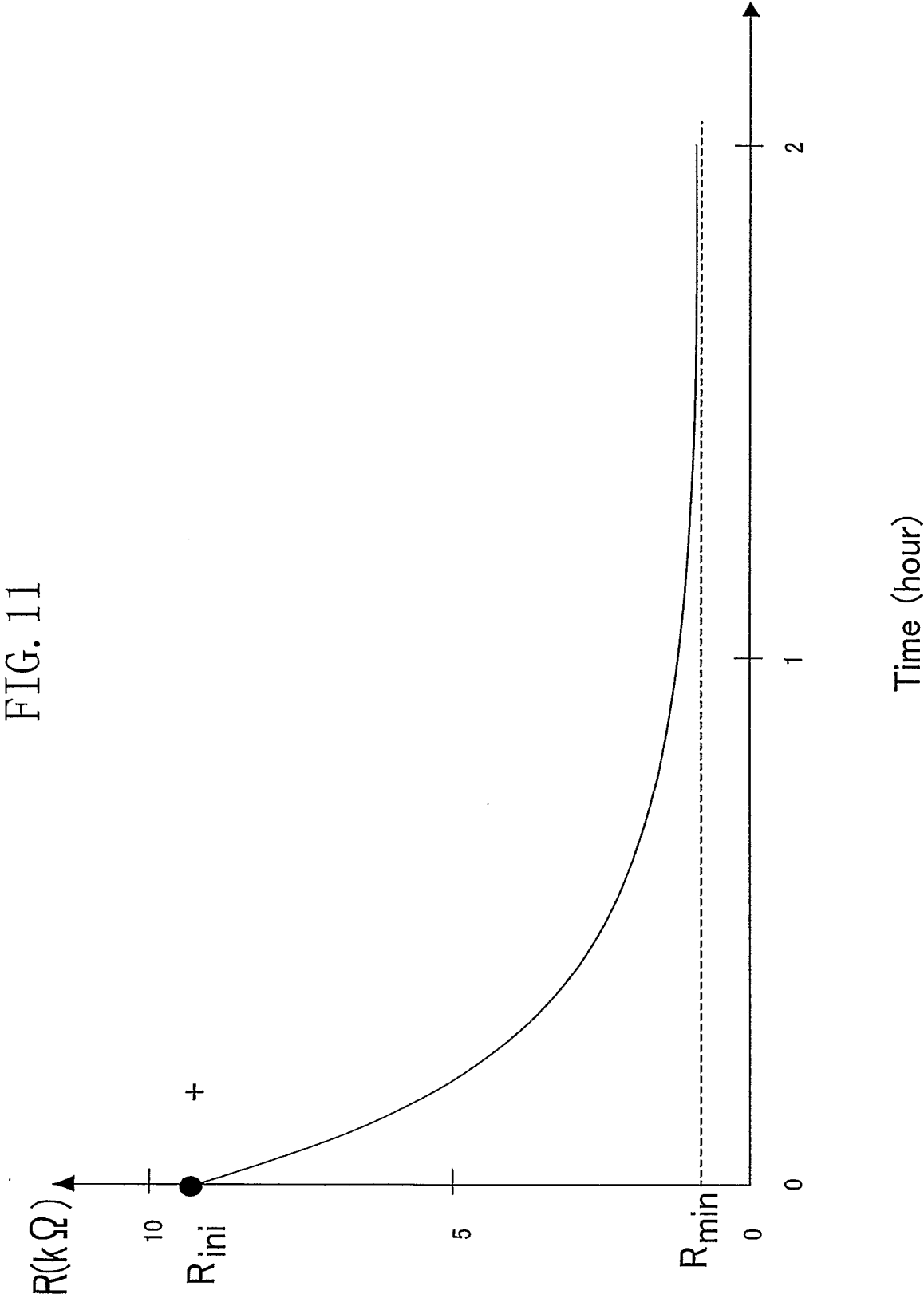
FIG. 9



9/34

FIG. 10





11/34

FIG. 12

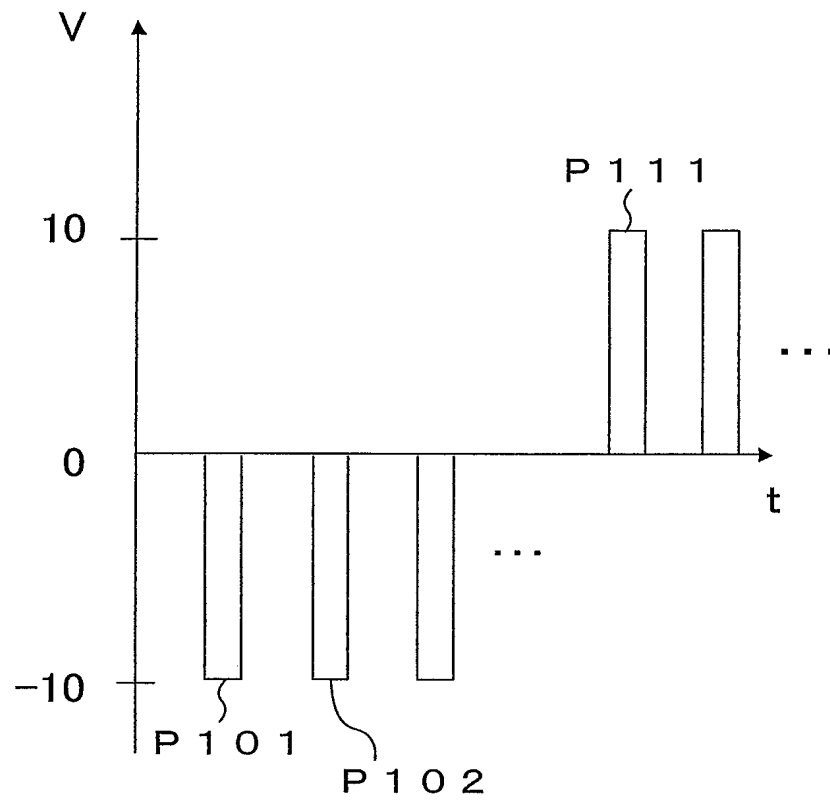
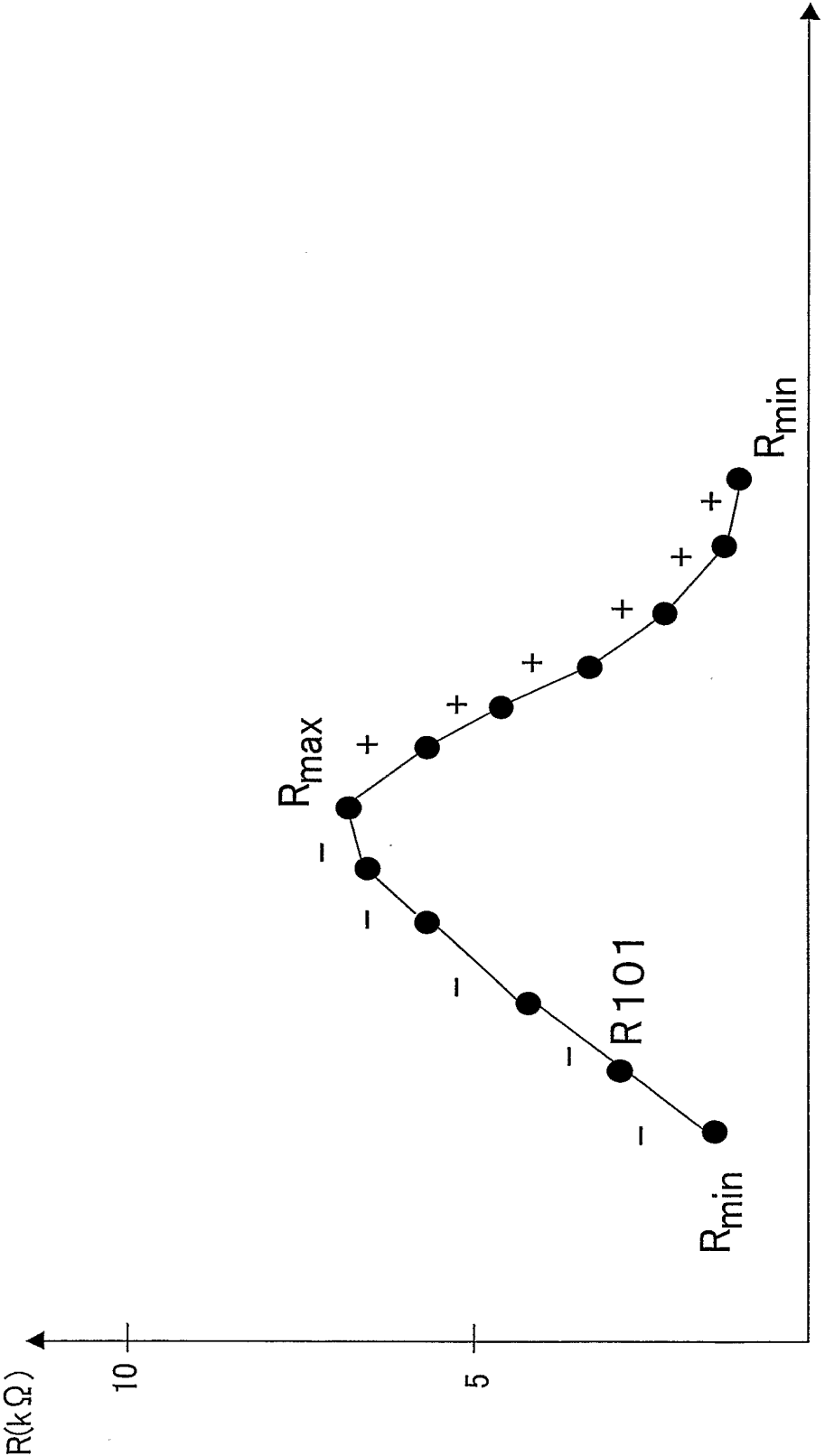
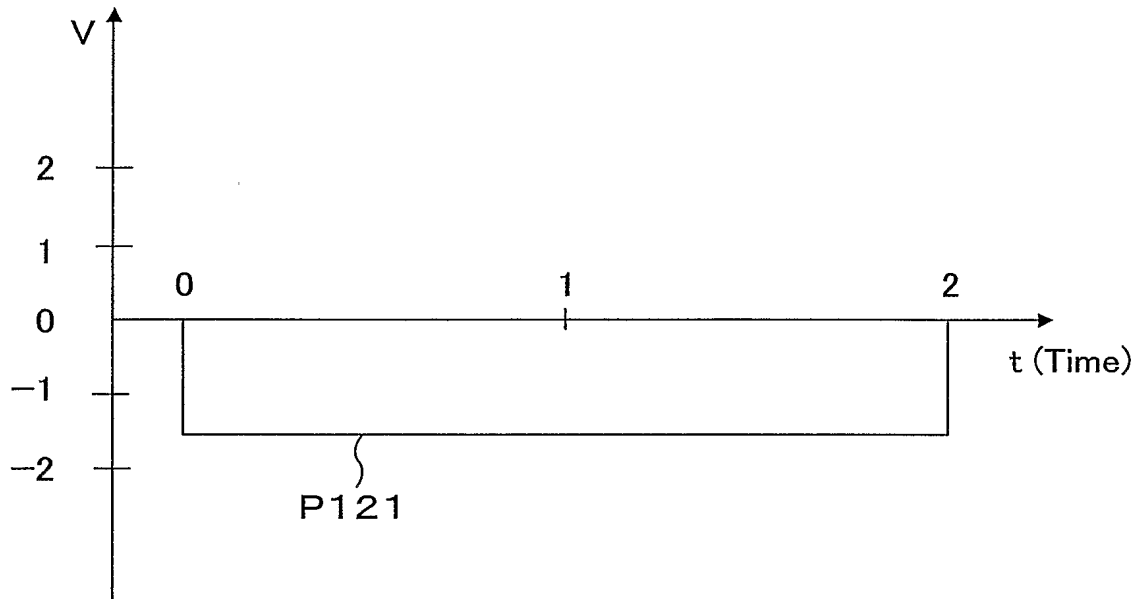


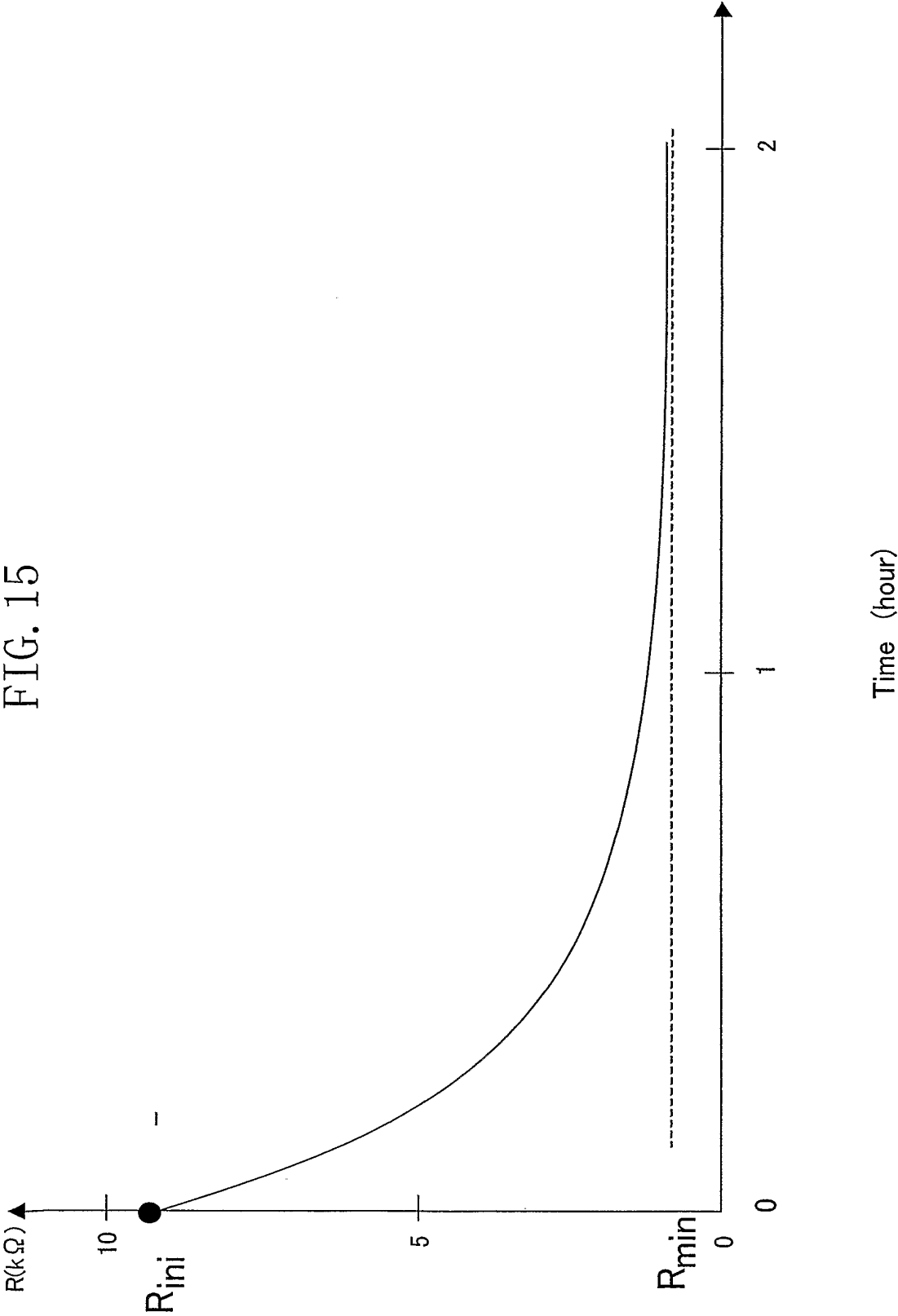
FIG. 13



13/34

FIG. 14





15/34

FIG. 16

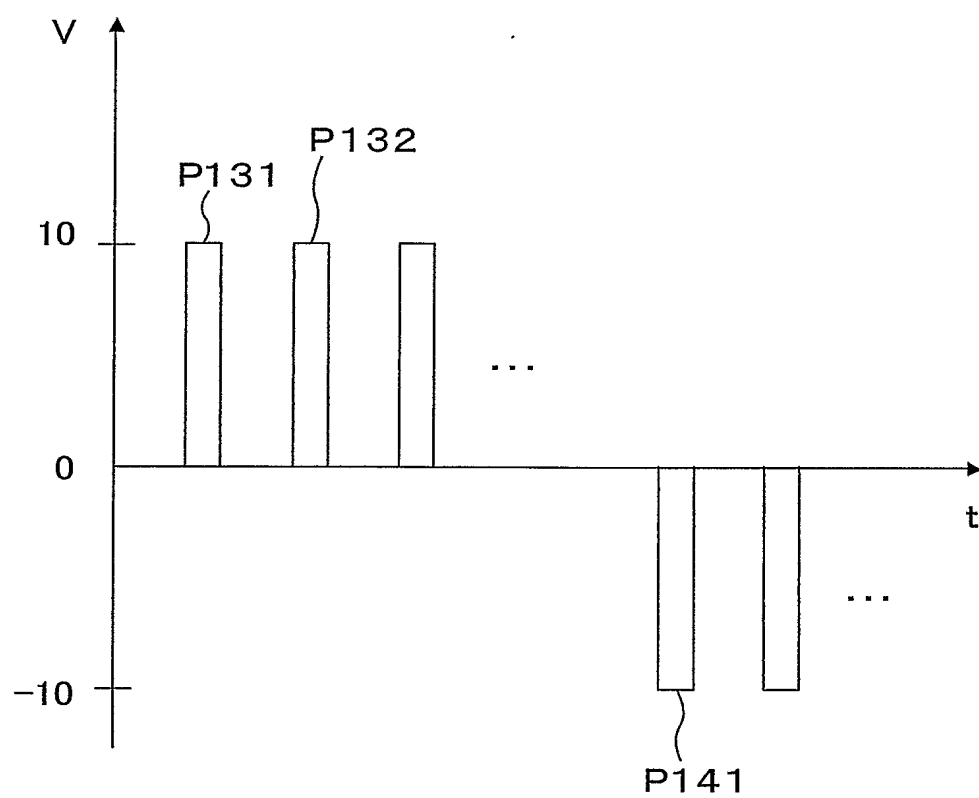
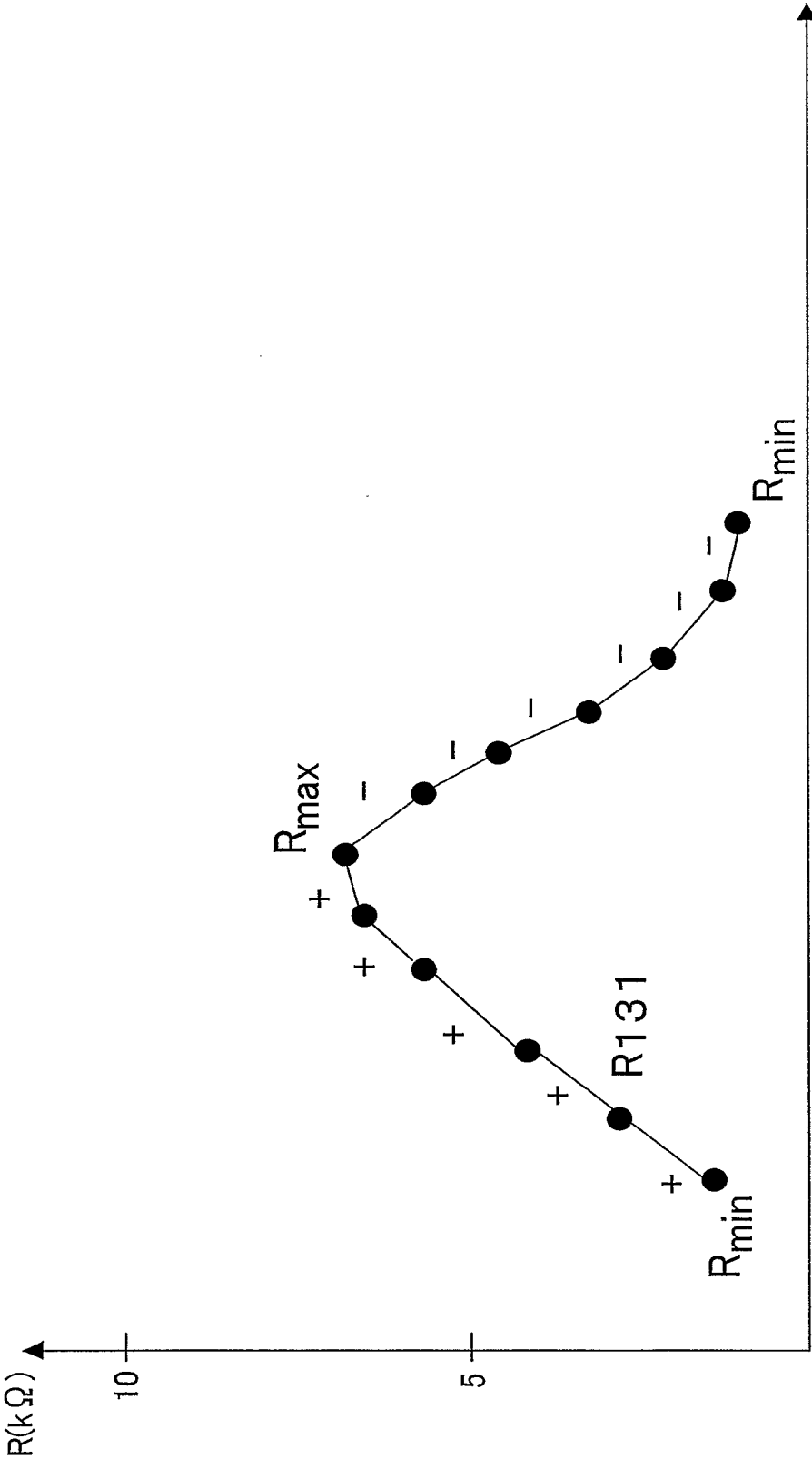
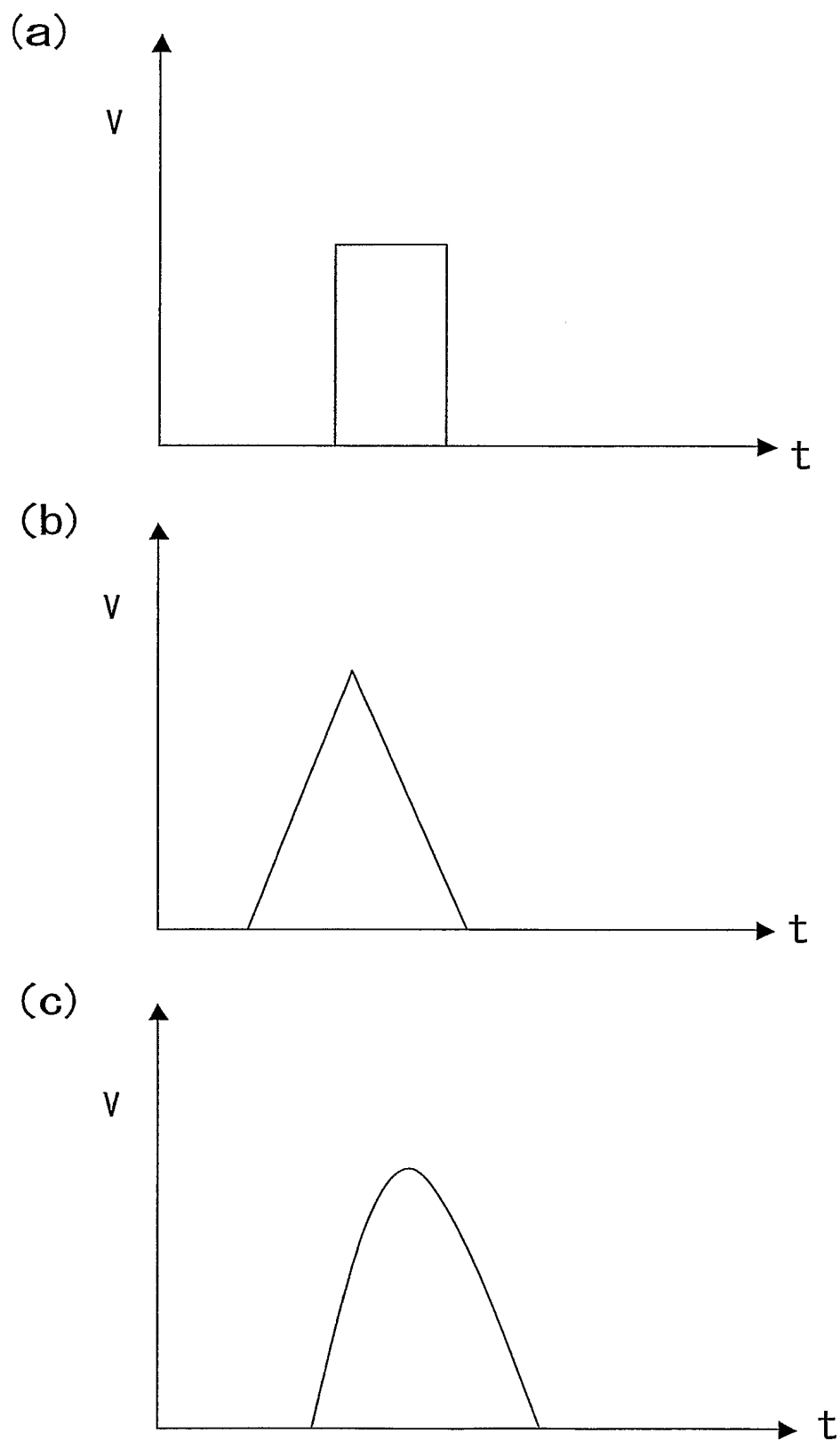


FIG. 17



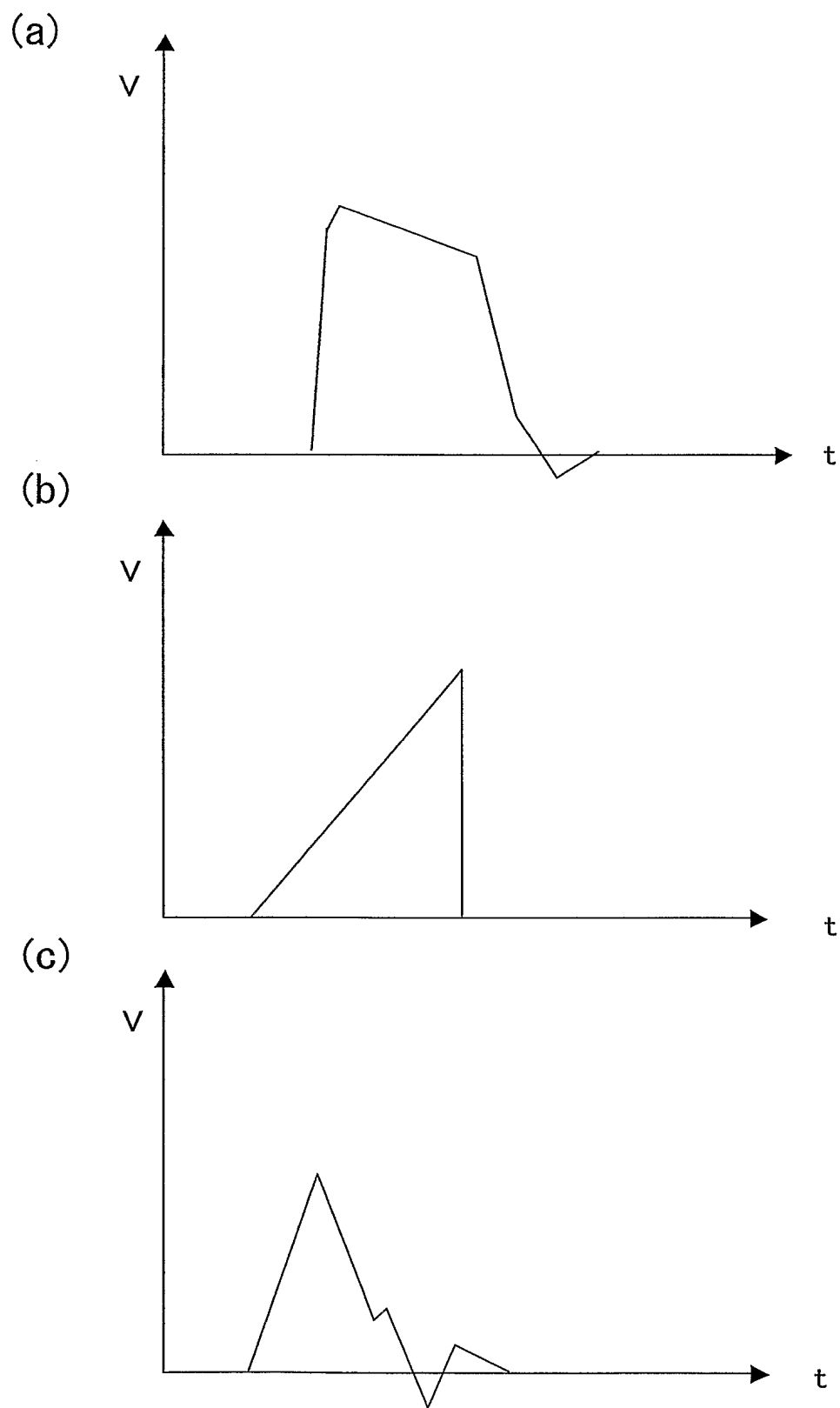
17/34

FIG. 18



18/34

FIG. 19



19/34

FIG. 20

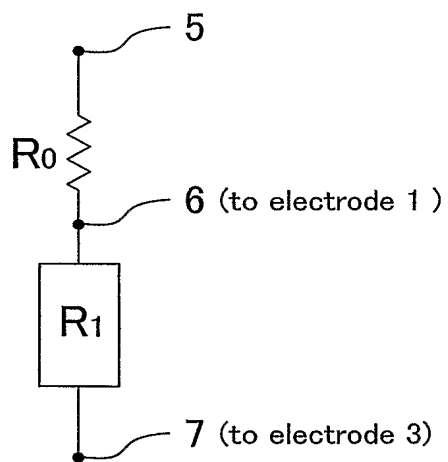
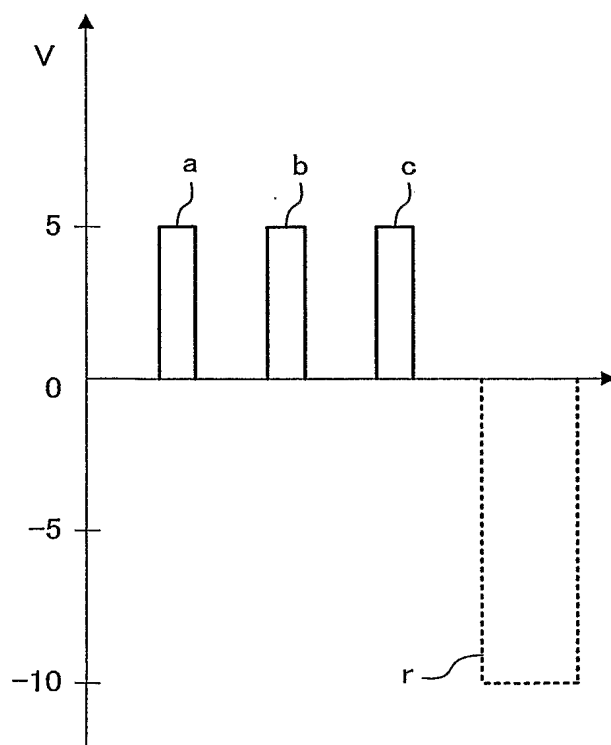


FIG. 21



20/34

FIG. 22

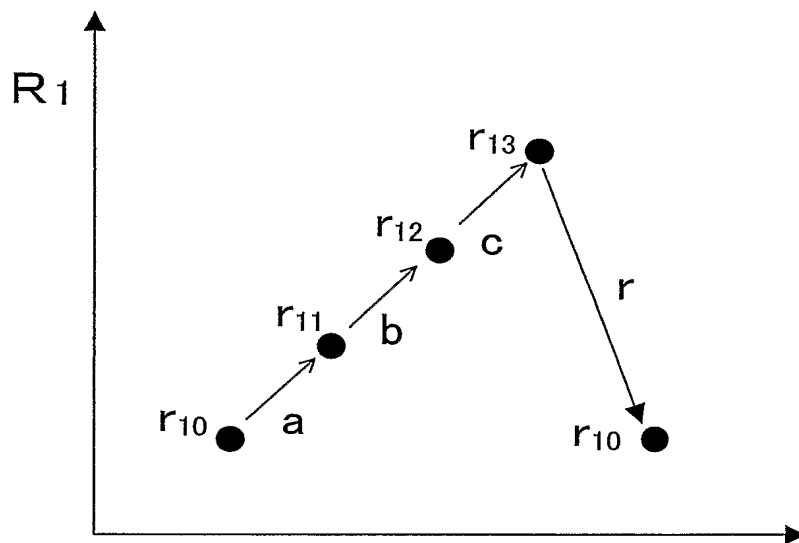
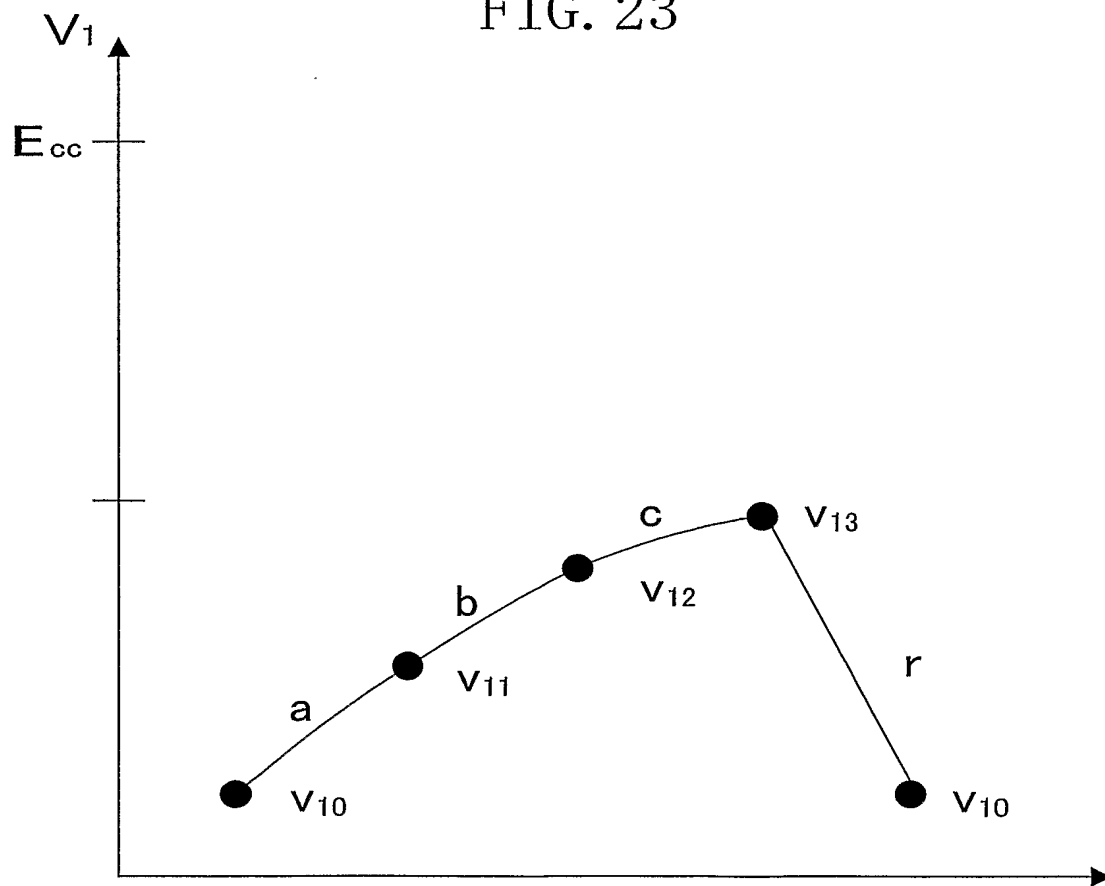


FIG. 23



21/34

FIG. 24

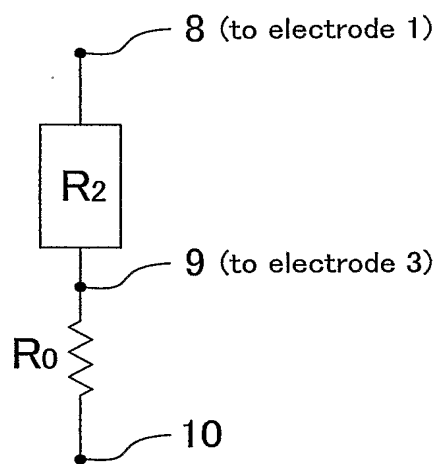


FIG. 25

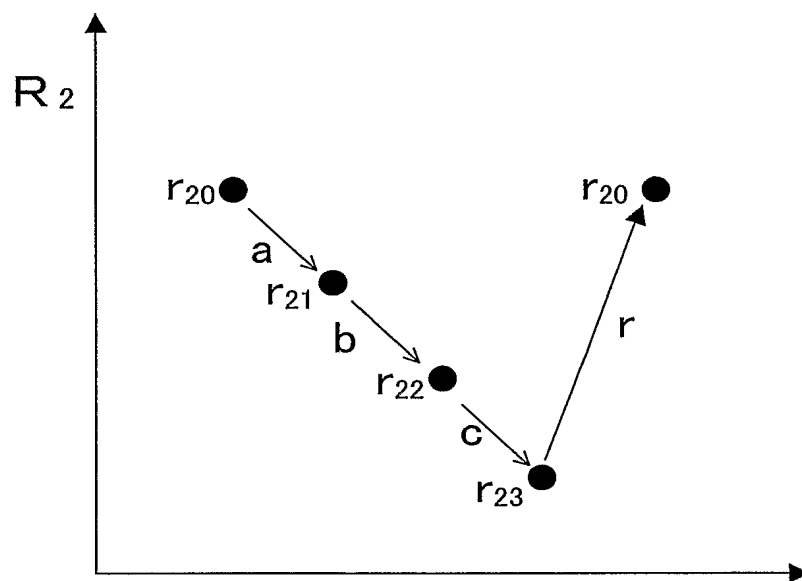
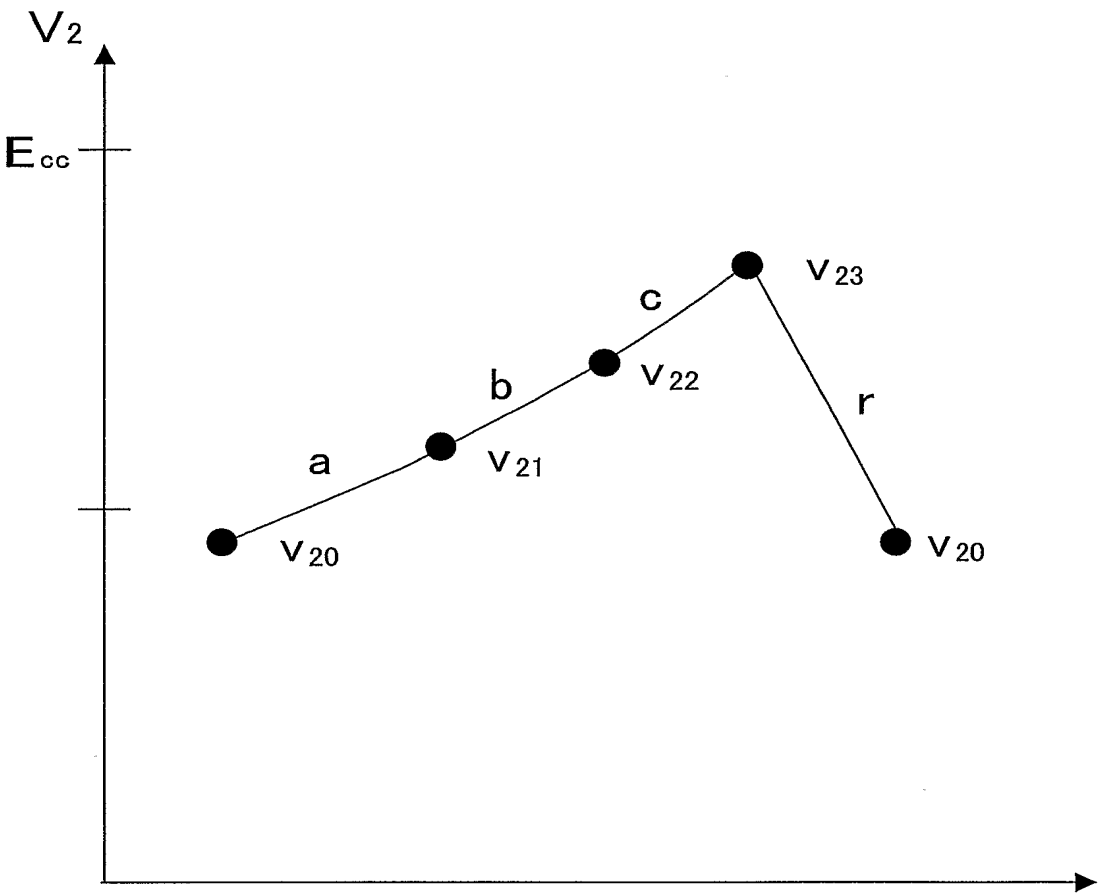
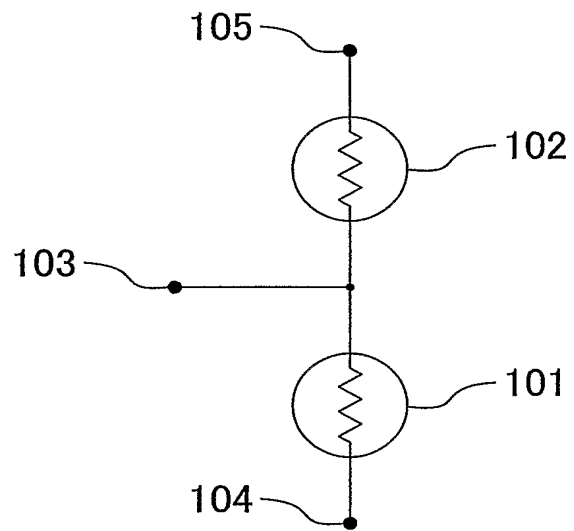


FIG. 26



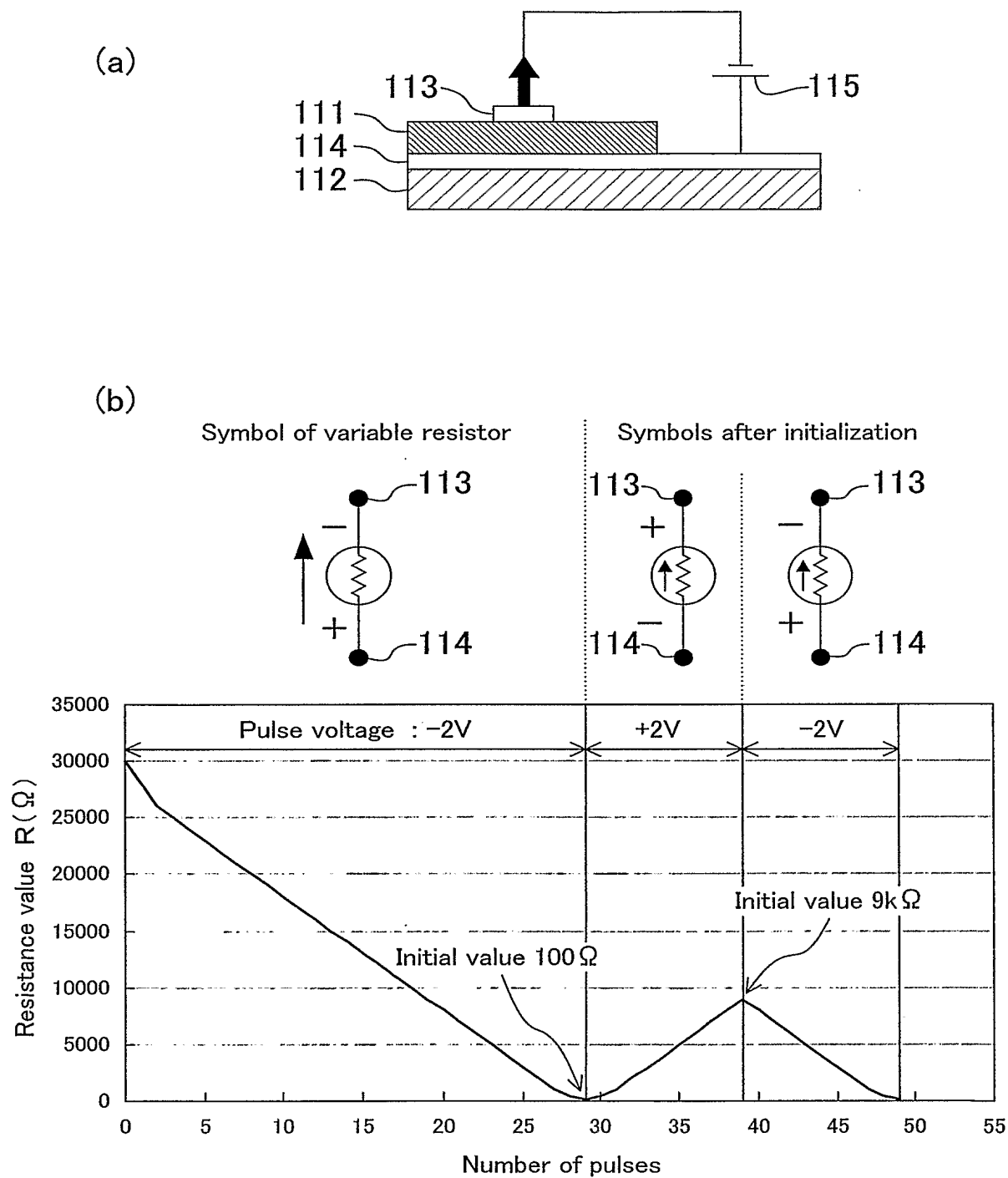
23/34

FIG. 27



24/34

FIG. 28



25/34

FIG. 29

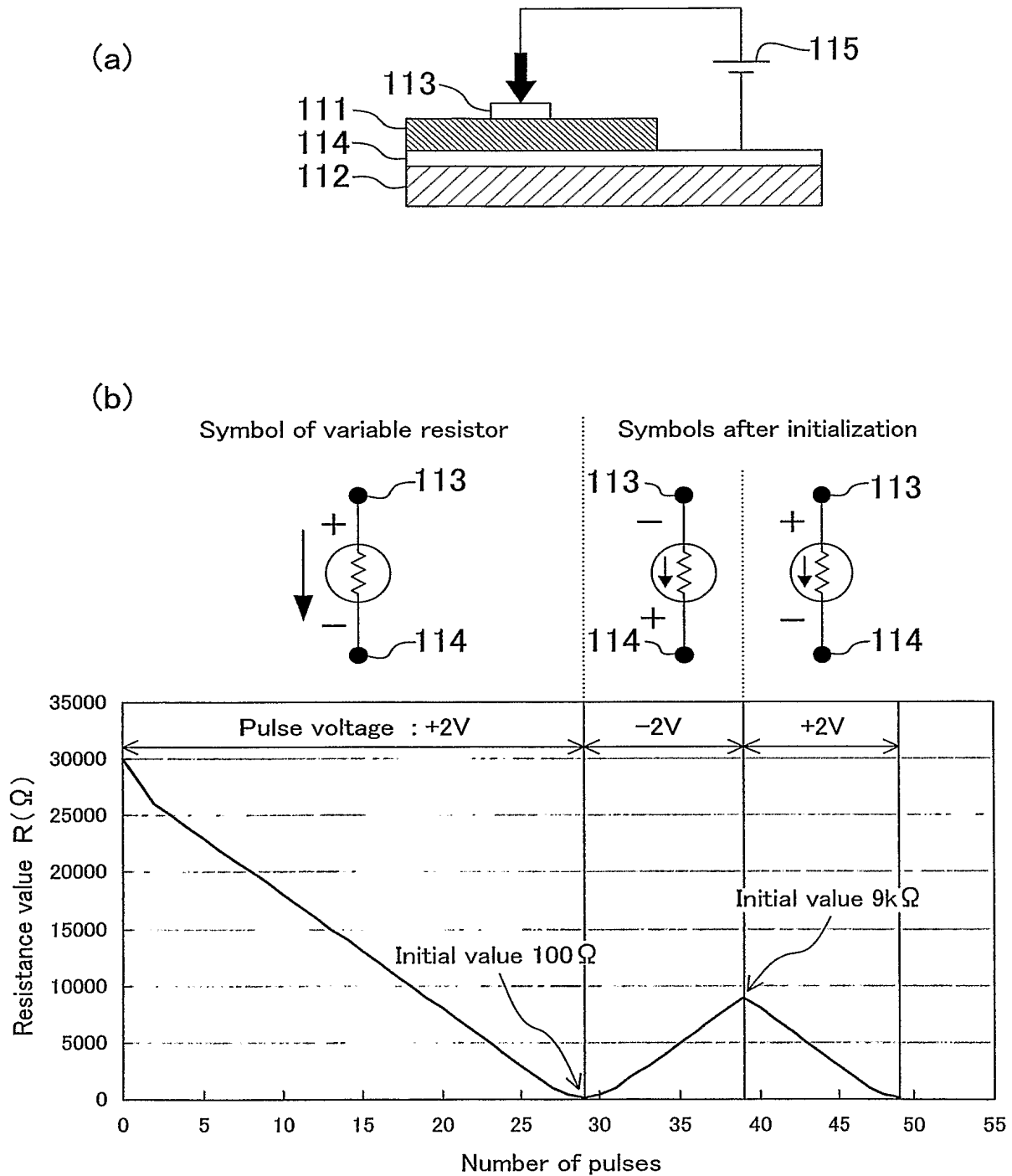
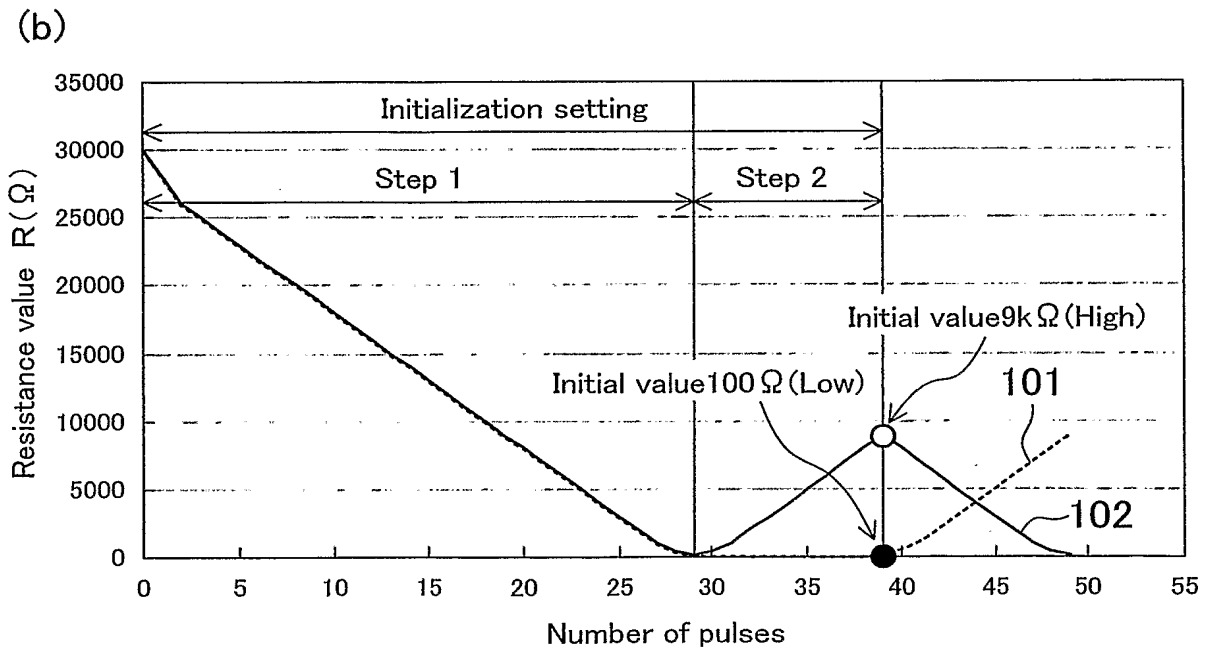
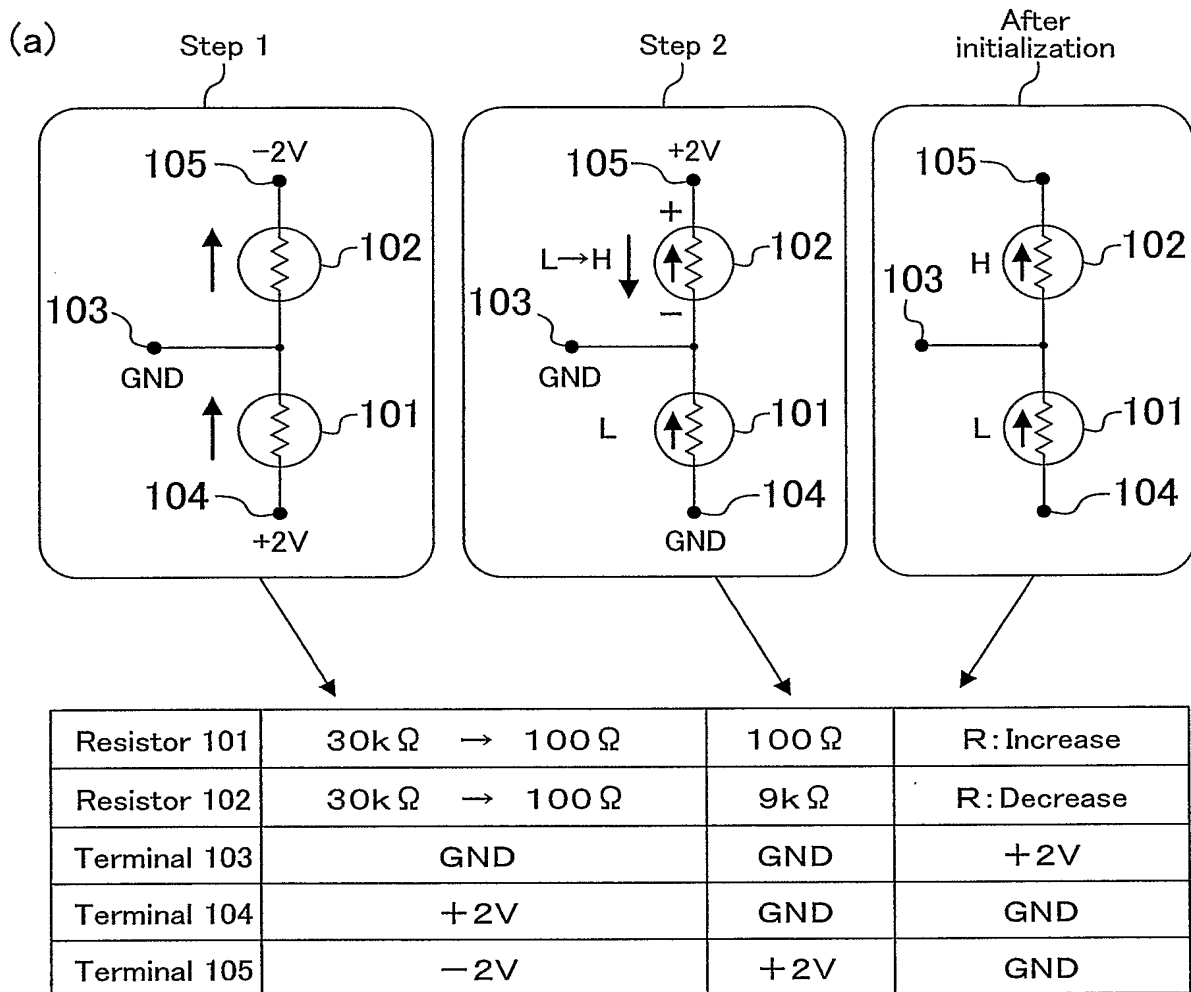


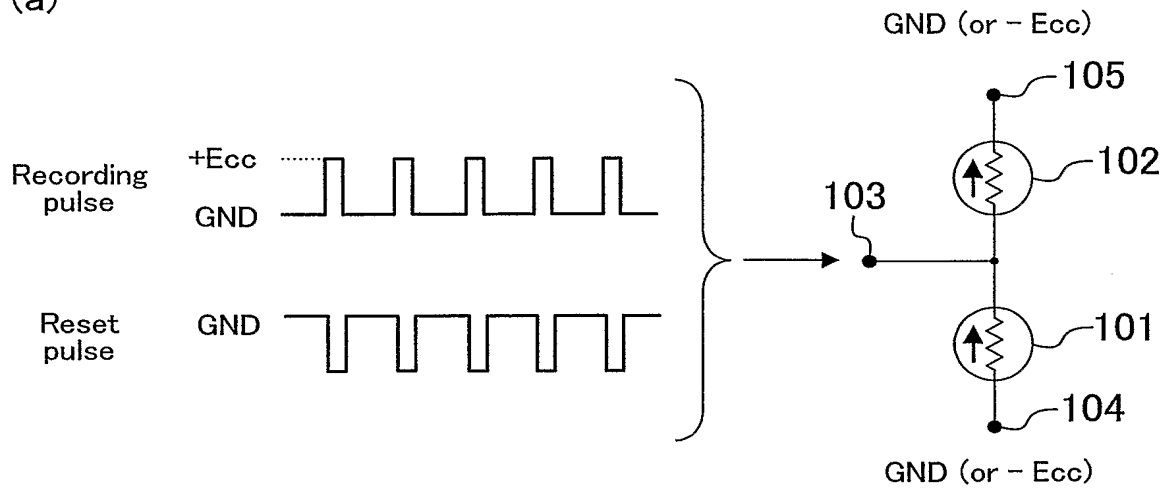
FIG. 30



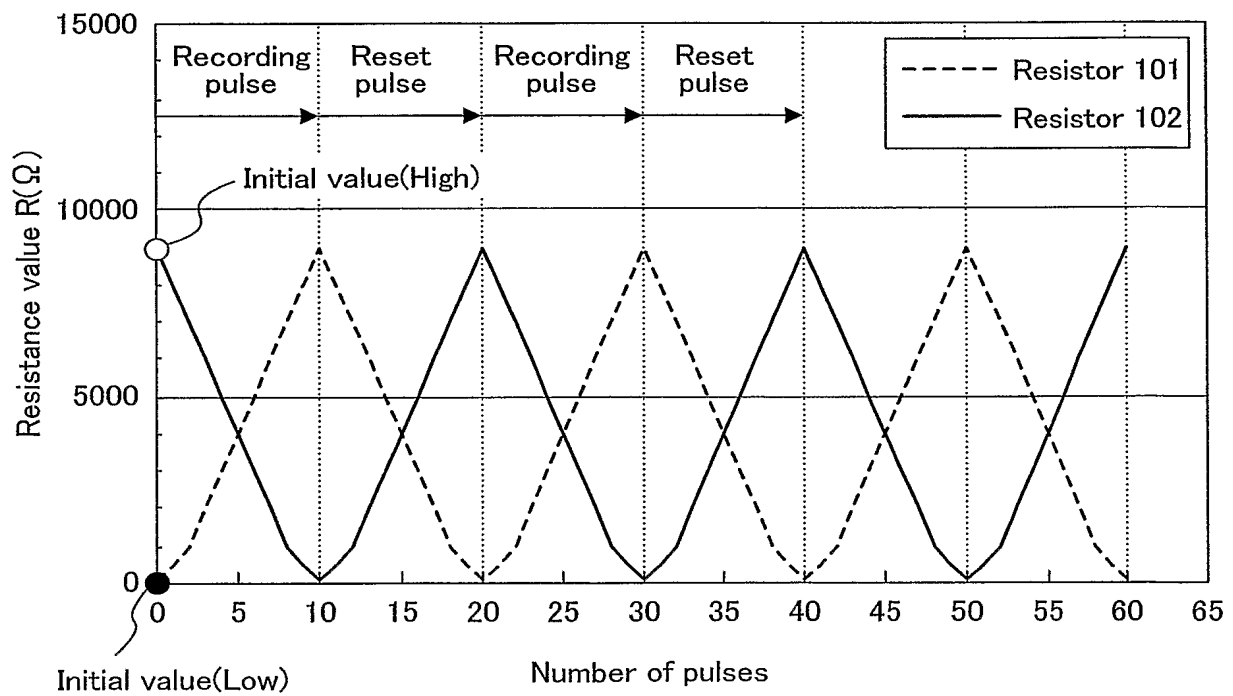
27/34

FIG. 31

(a)

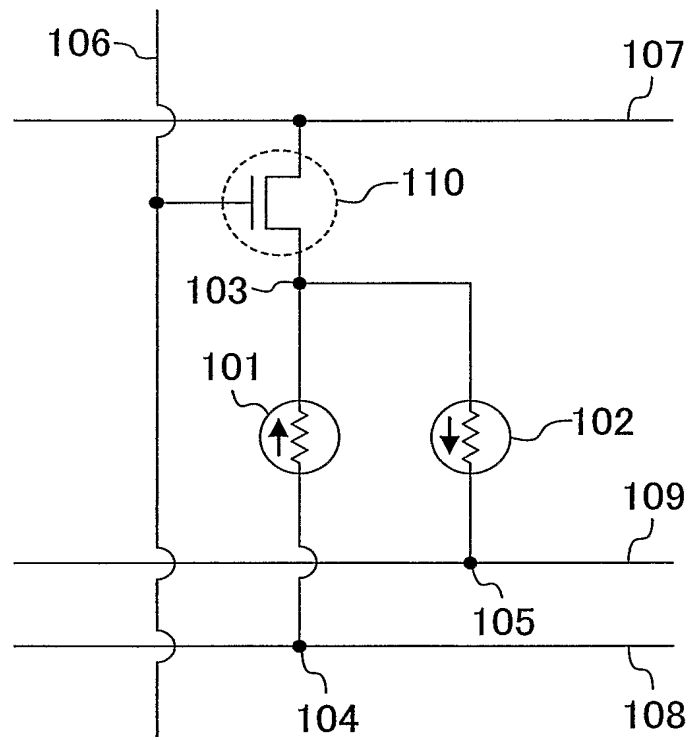


(b)



28/34

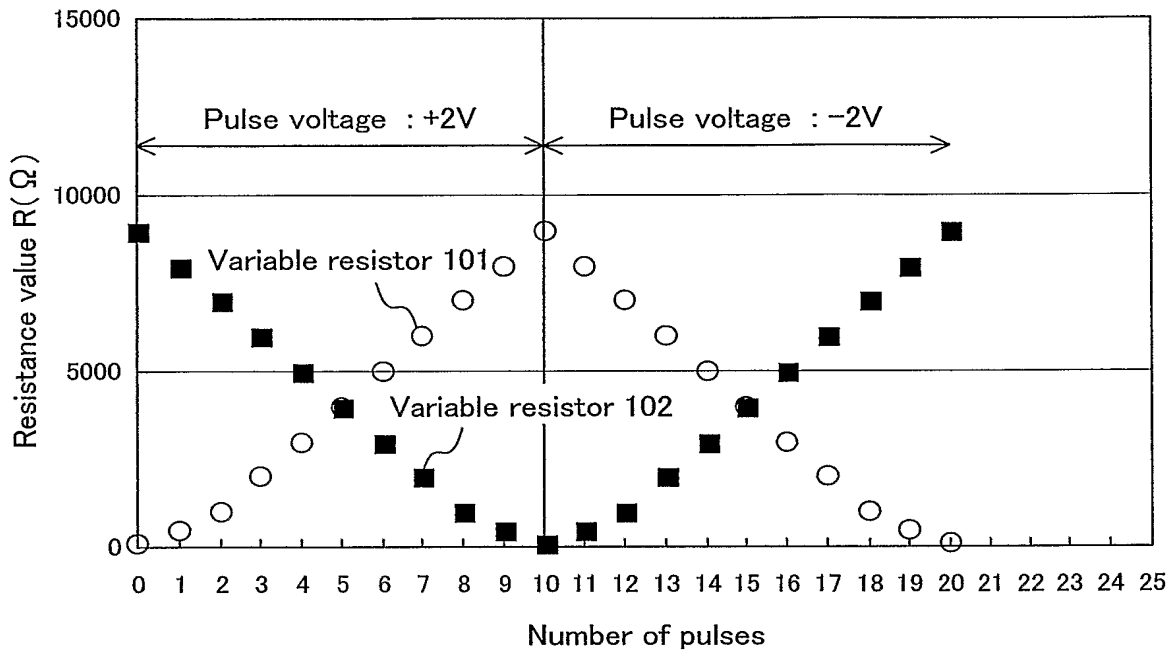
FIG. 32



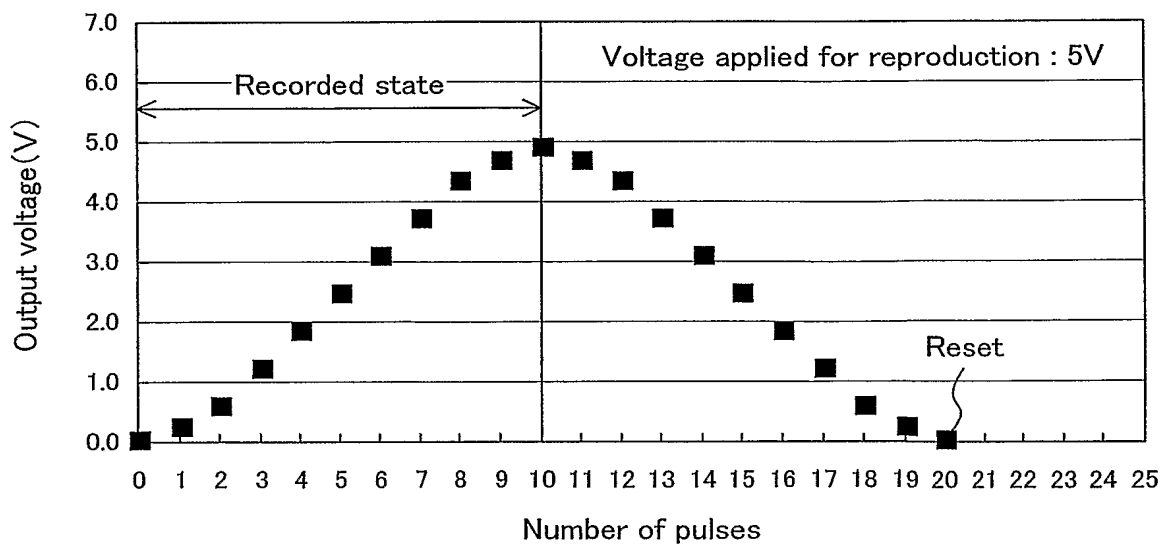
29/34

FIG. 33

(a) Resistance variation over number of pulses



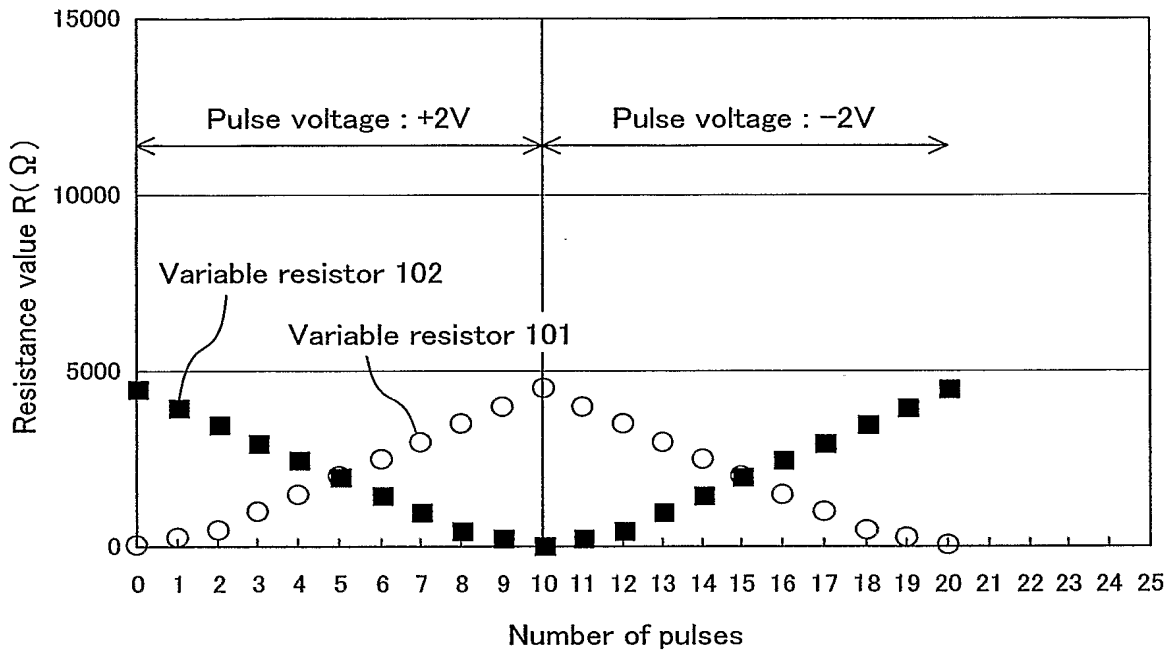
(b) Output voltage when reading recorded state



30/34

FIG. 34

(a) Resistance variation over number of pulses



(b) Output voltage when reading recorded state

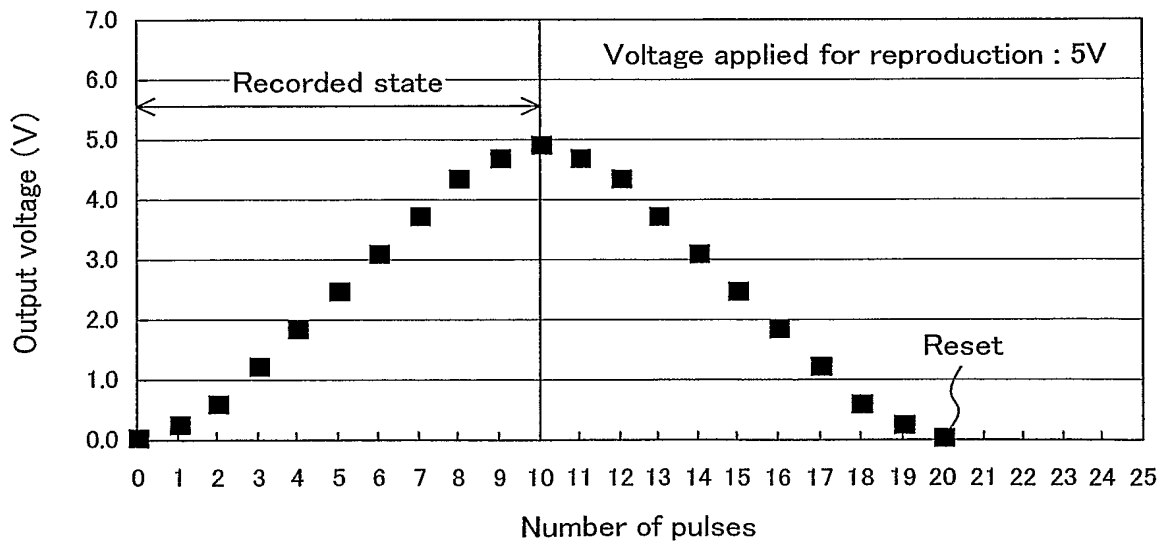


FIG. 35

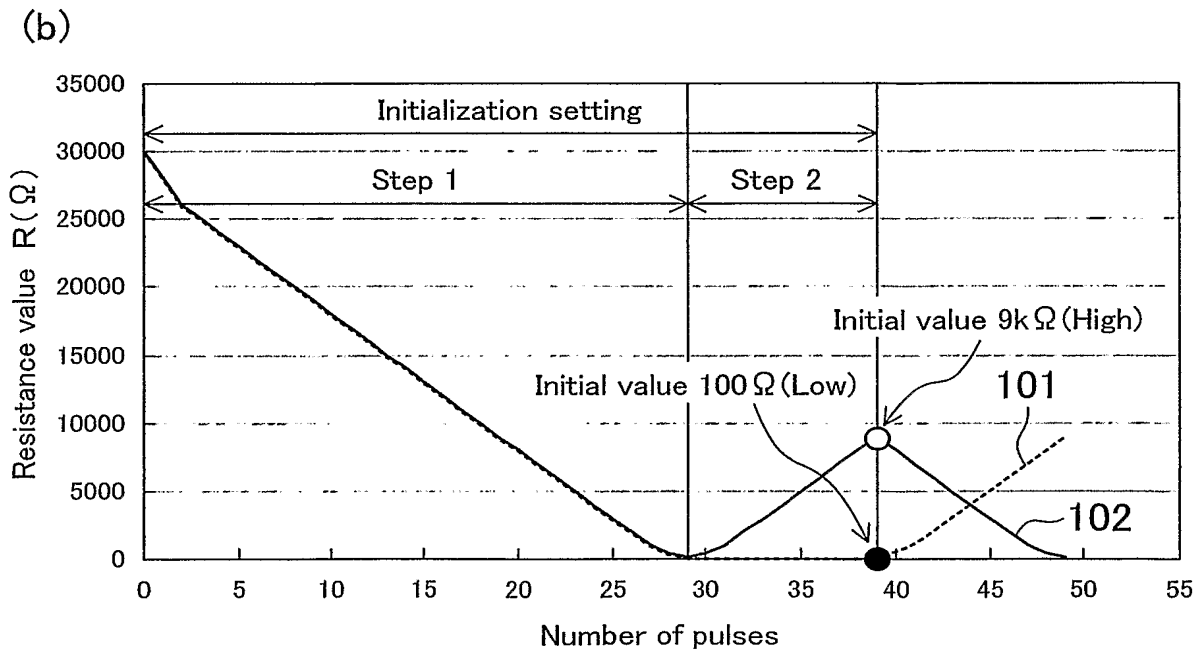
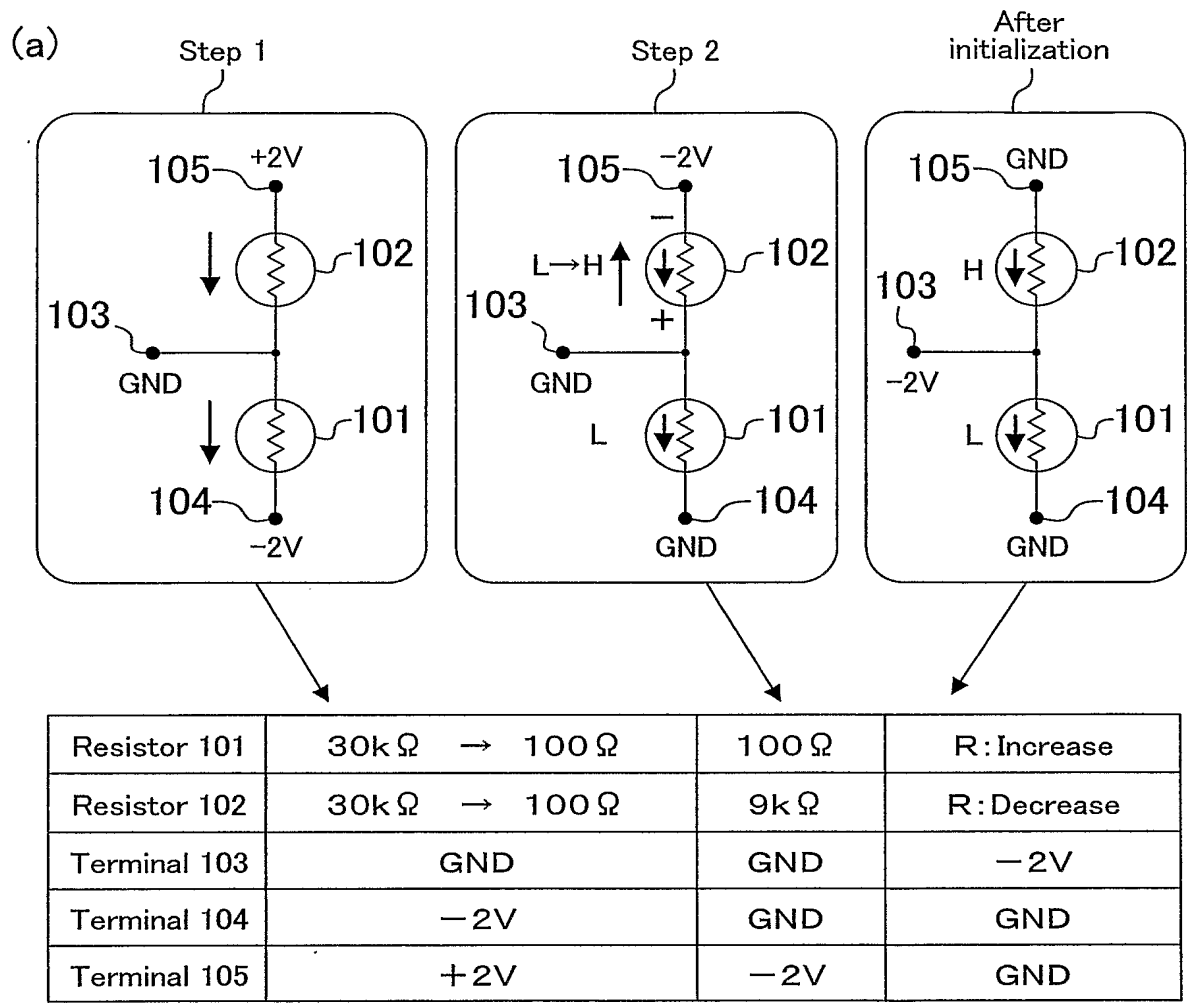
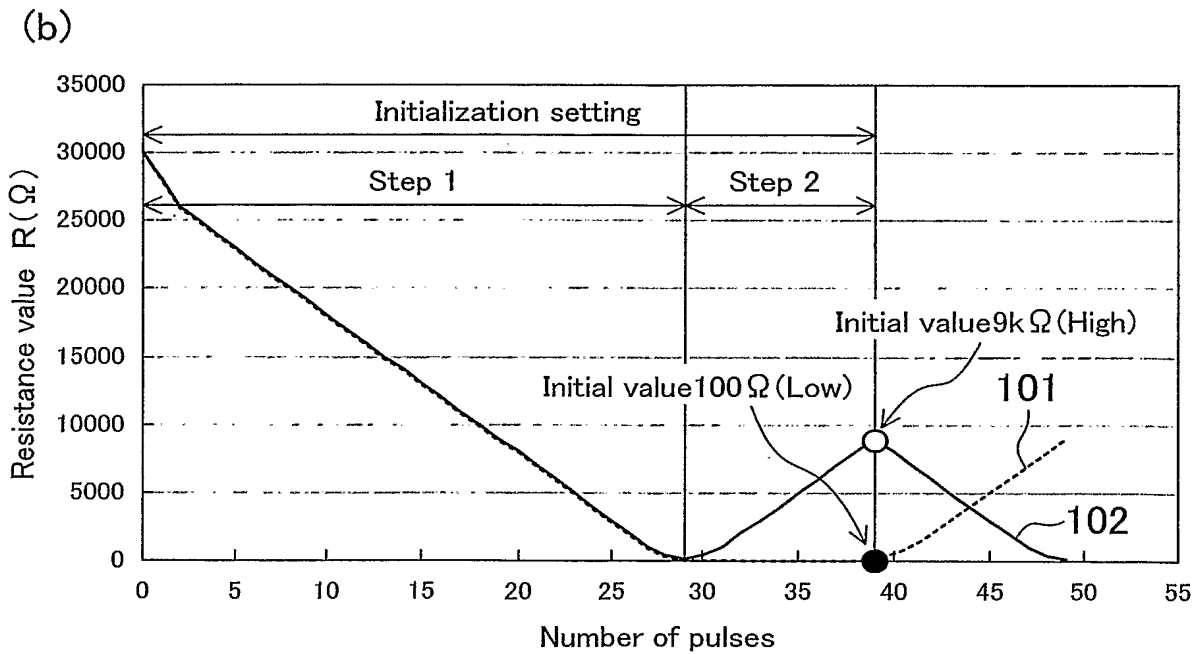
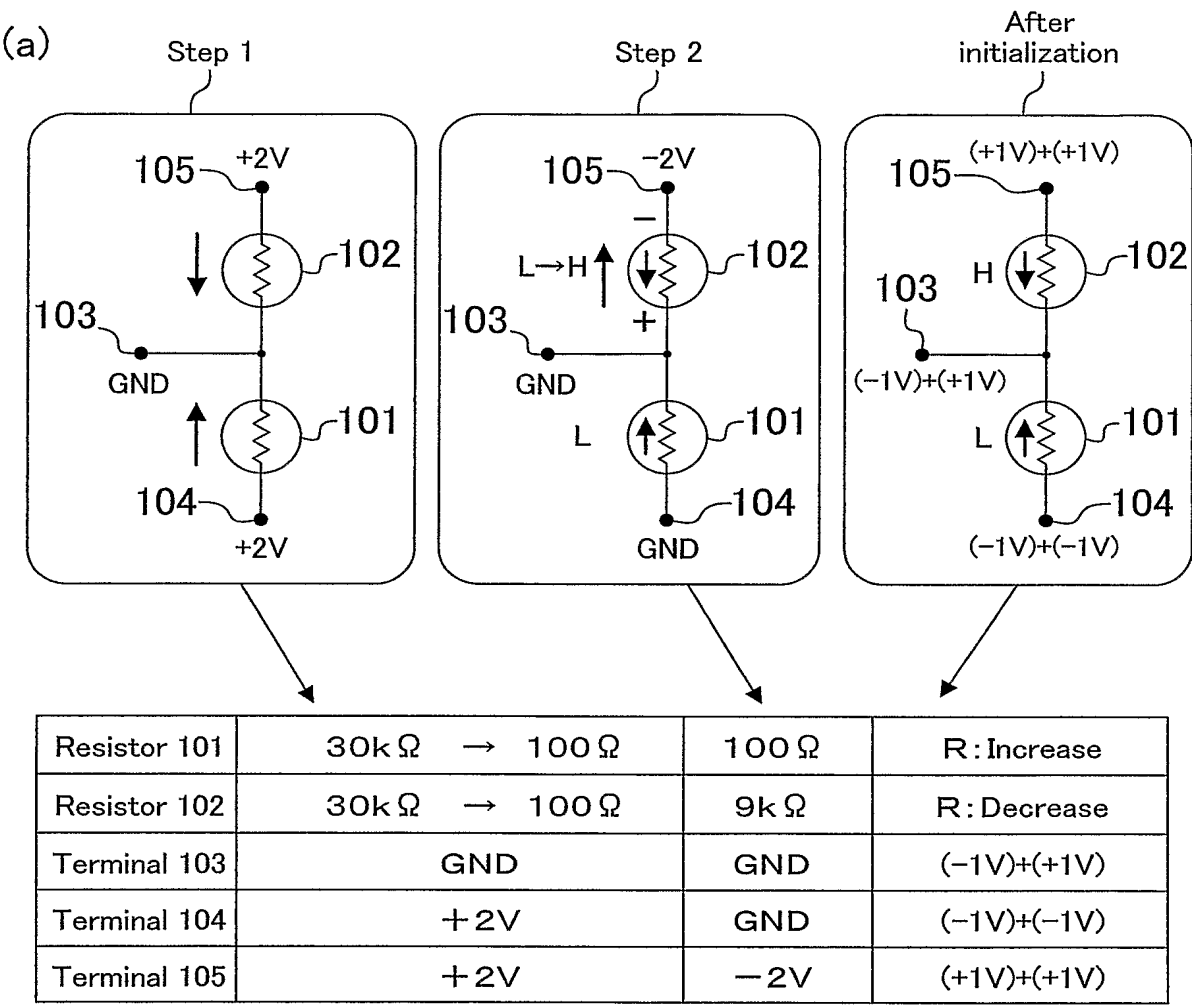
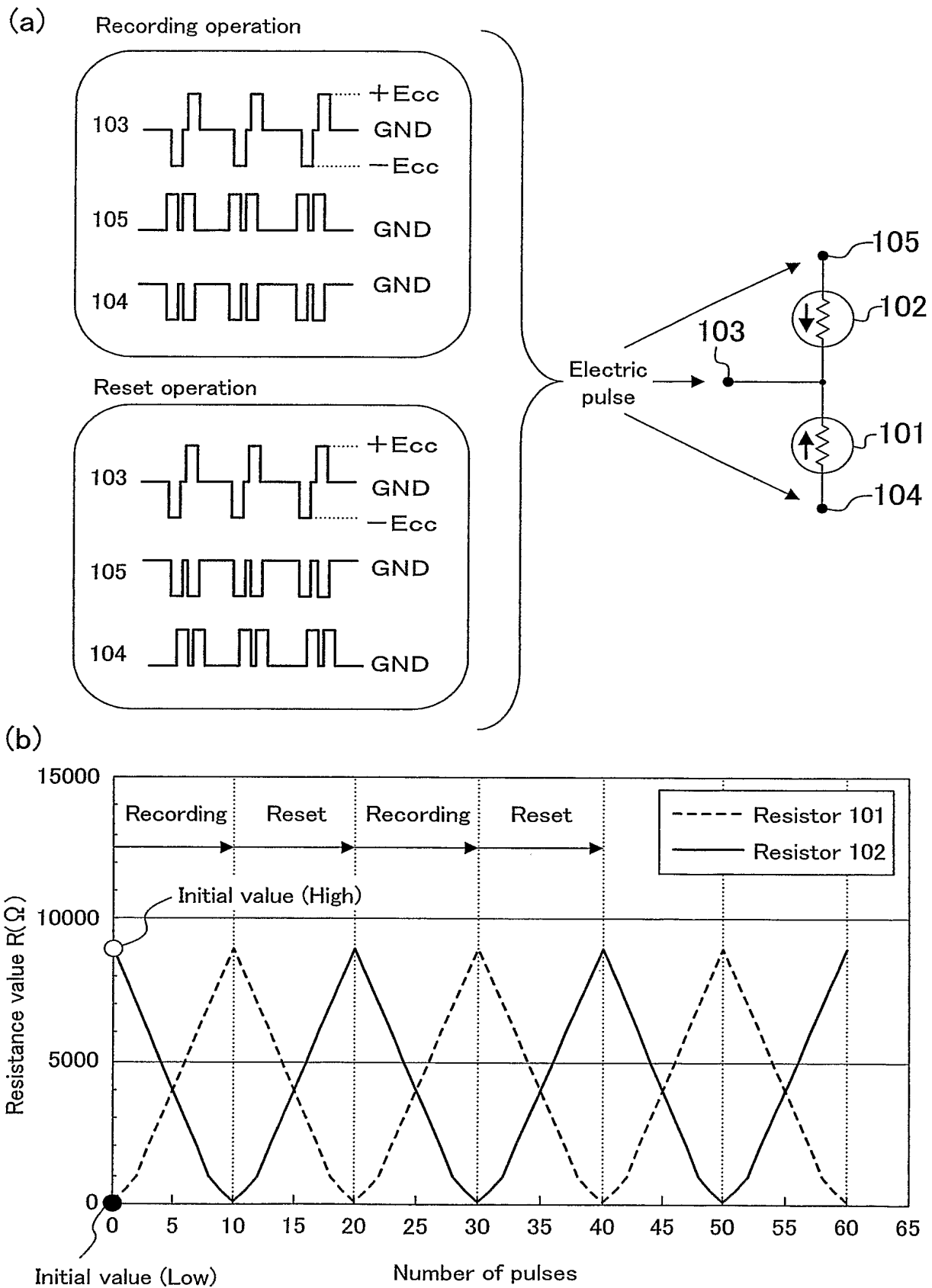


FIG. 36



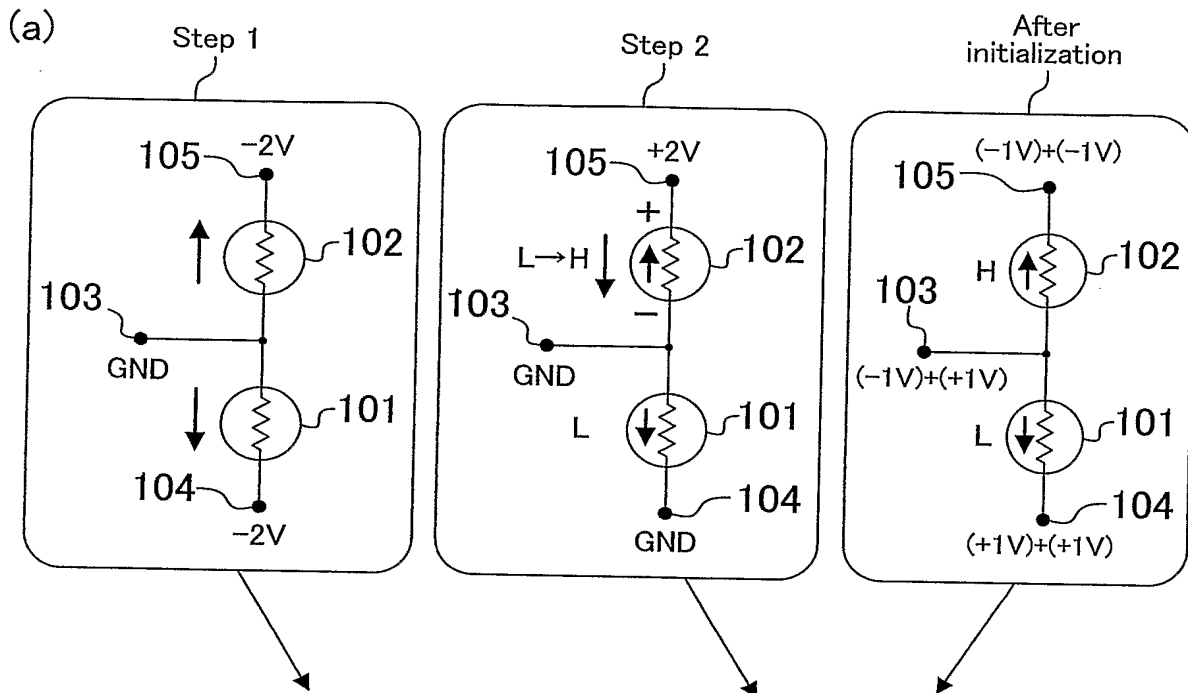
33/34

FIG. 37



34/34

FIG. 38



Resistor 101	$30\text{k}\Omega \rightarrow 100\Omega$	100Ω	R: Increase
Resistor 102	$30\text{k}\Omega \rightarrow 100\Omega$	$9\text{k}\Omega$	R: Decrease
Terminal 103	GND	GND	$(-1\text{V})+(+1\text{V})$
Terminal 104	-2V	GND	$(+1\text{V})+(+1\text{V})$
Terminal 105	-2V	$+2\text{V}$	$(-1\text{V})+(-1\text{V})$

